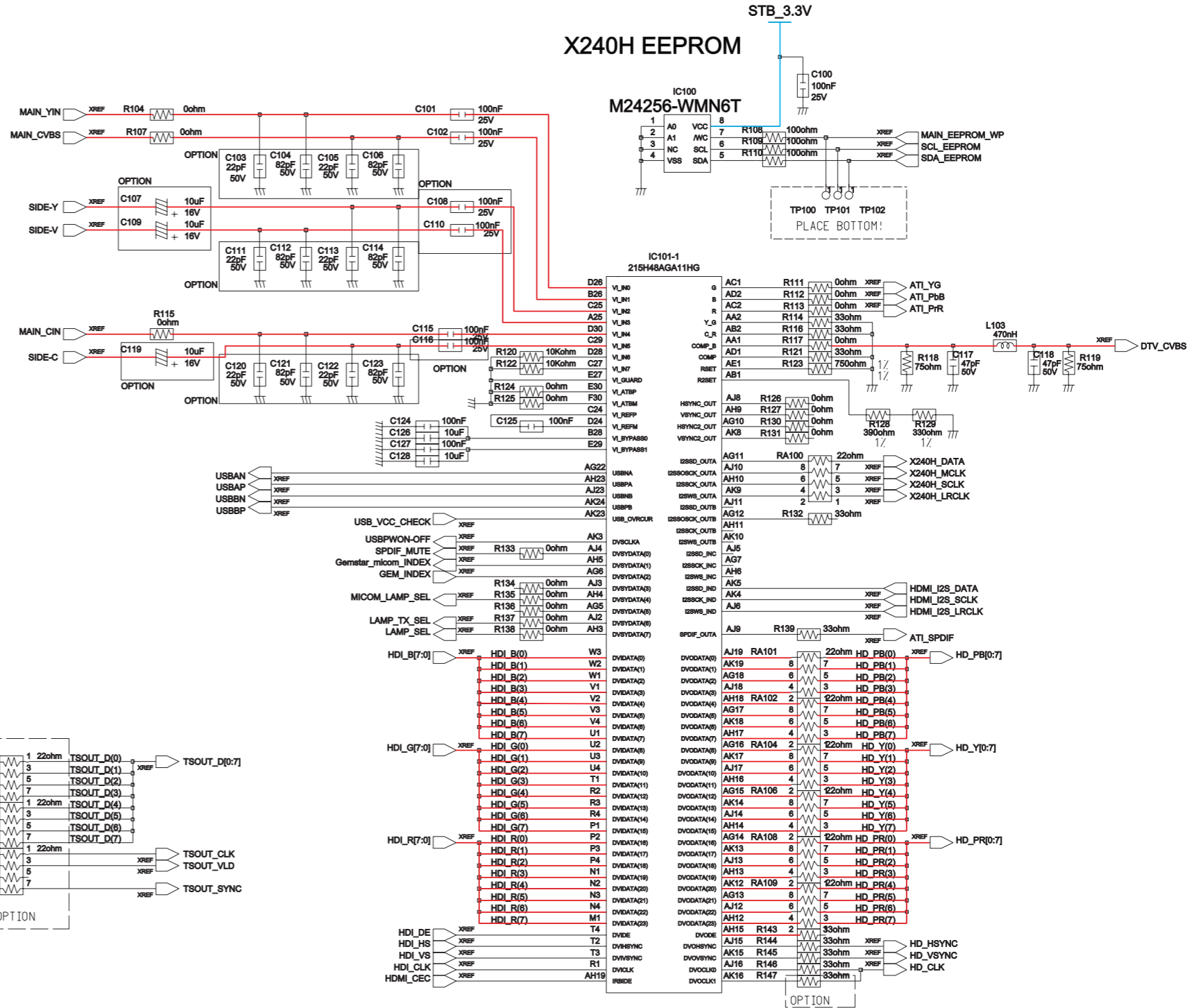
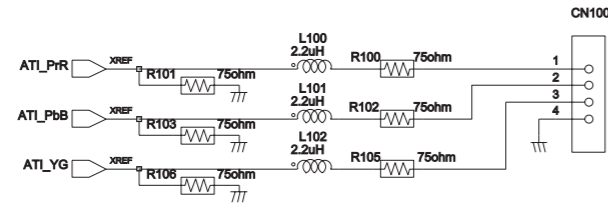
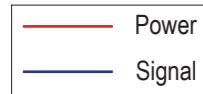


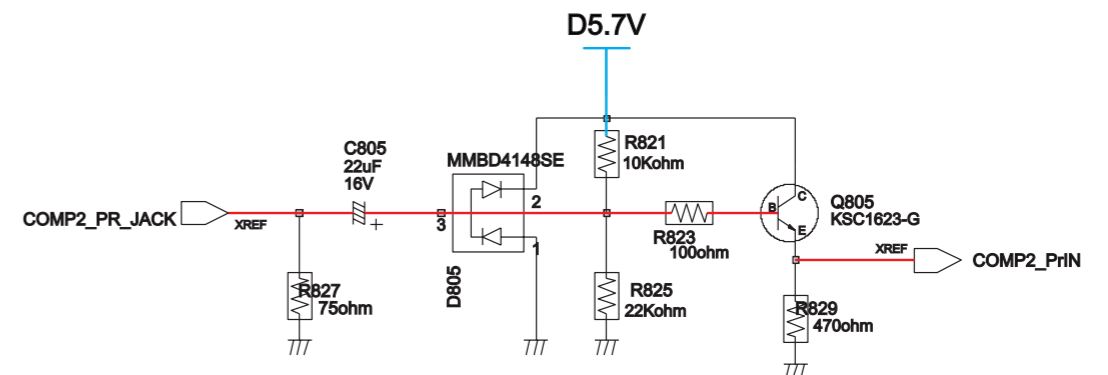
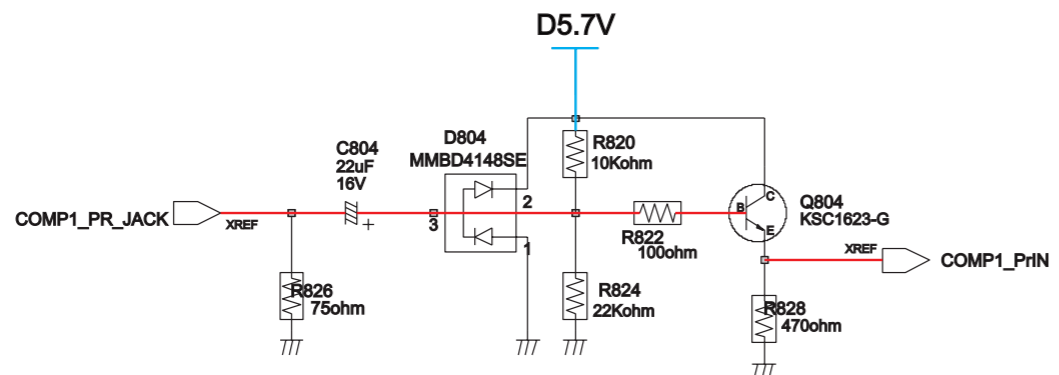
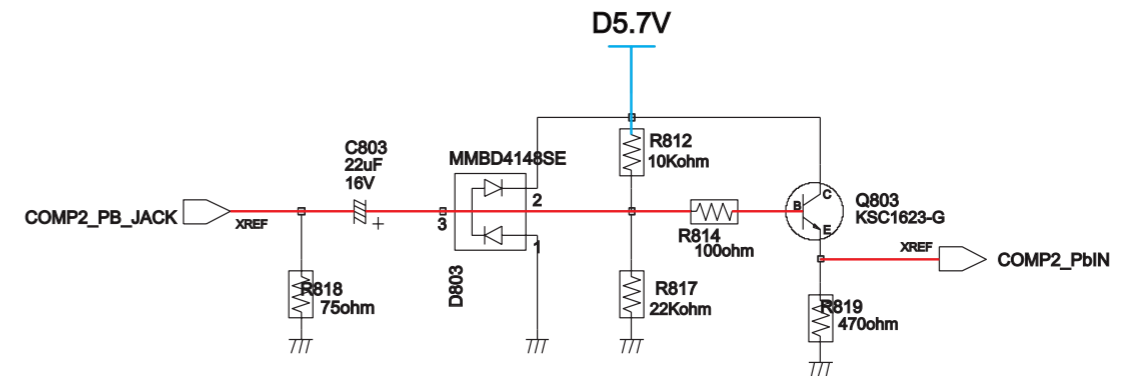
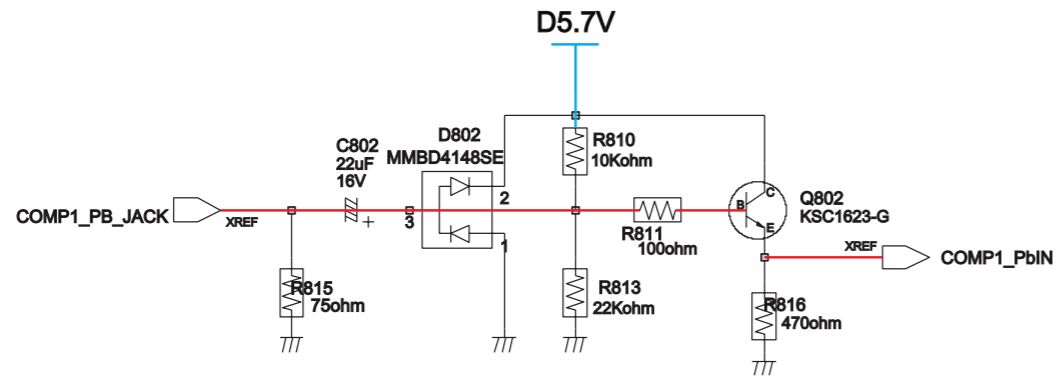
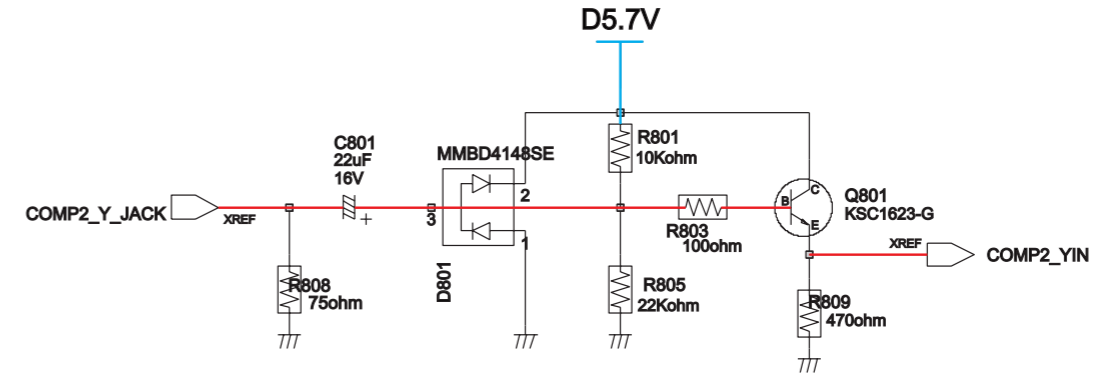
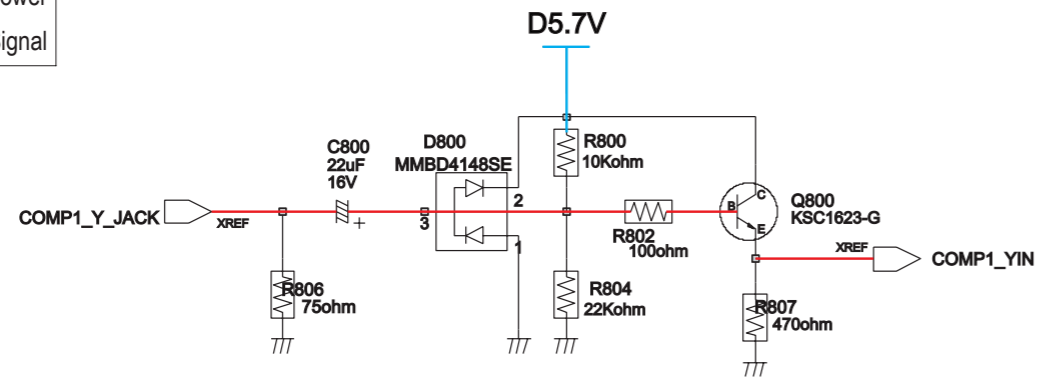
10. Schematic Diagram

10-1 Main Board

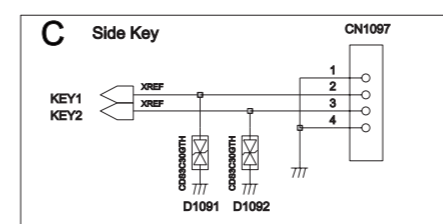
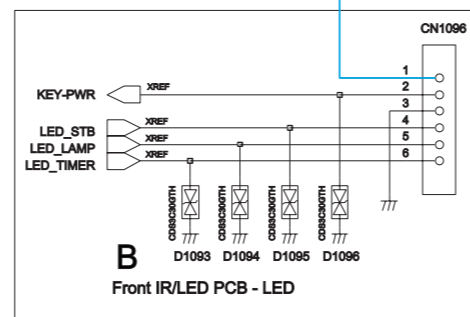
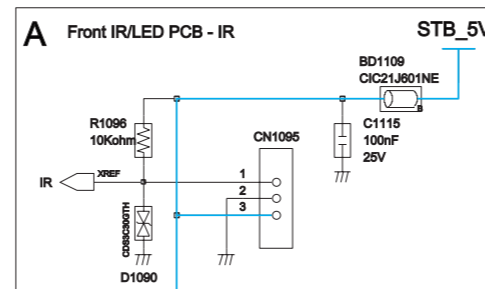
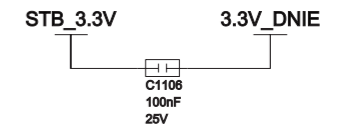
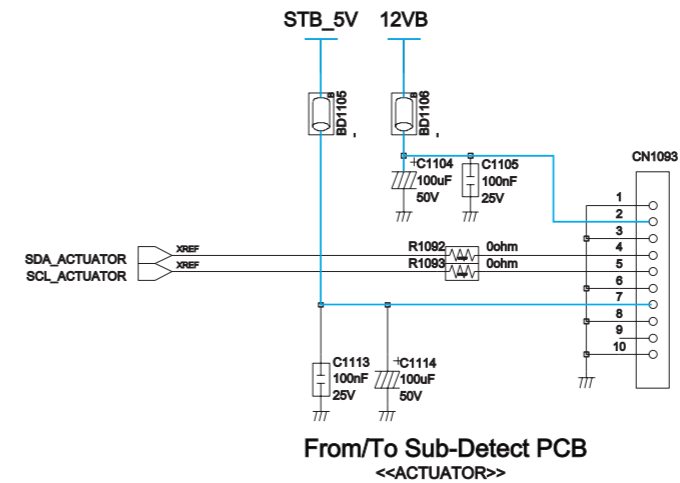
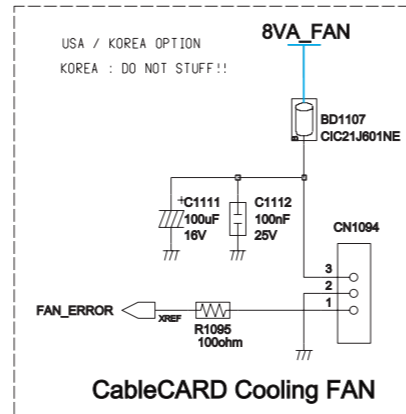
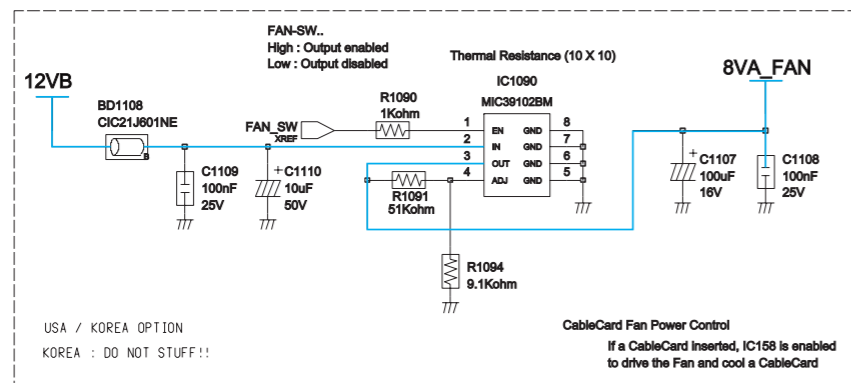
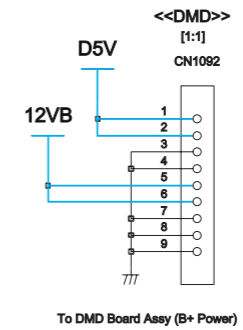
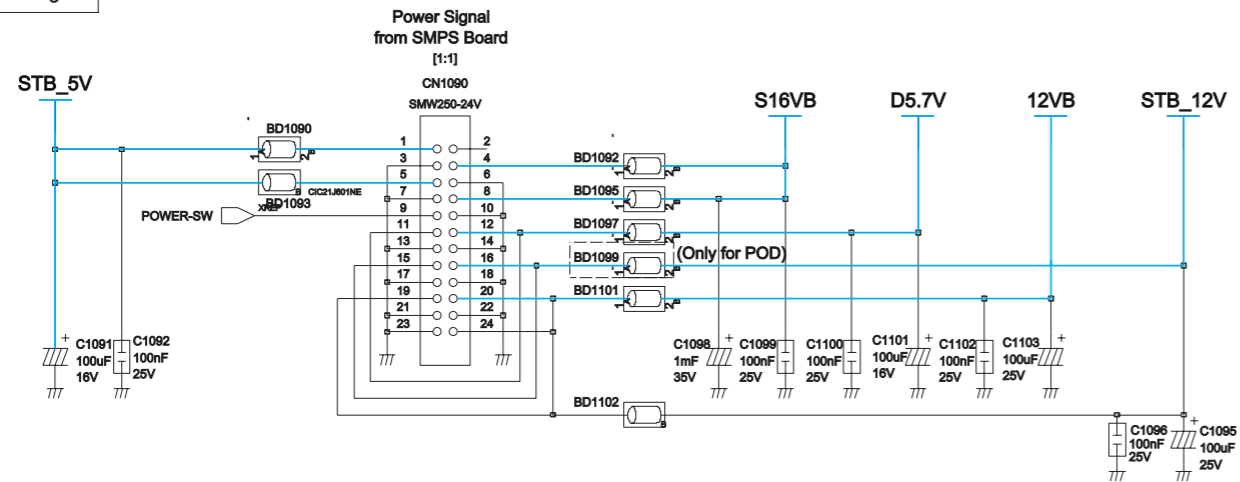
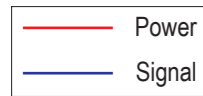
10-1-1 Main-1



10-1-2 Main-2



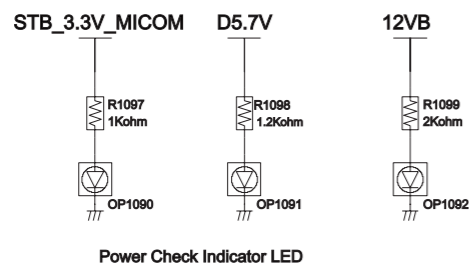
10-1-3 Main-3



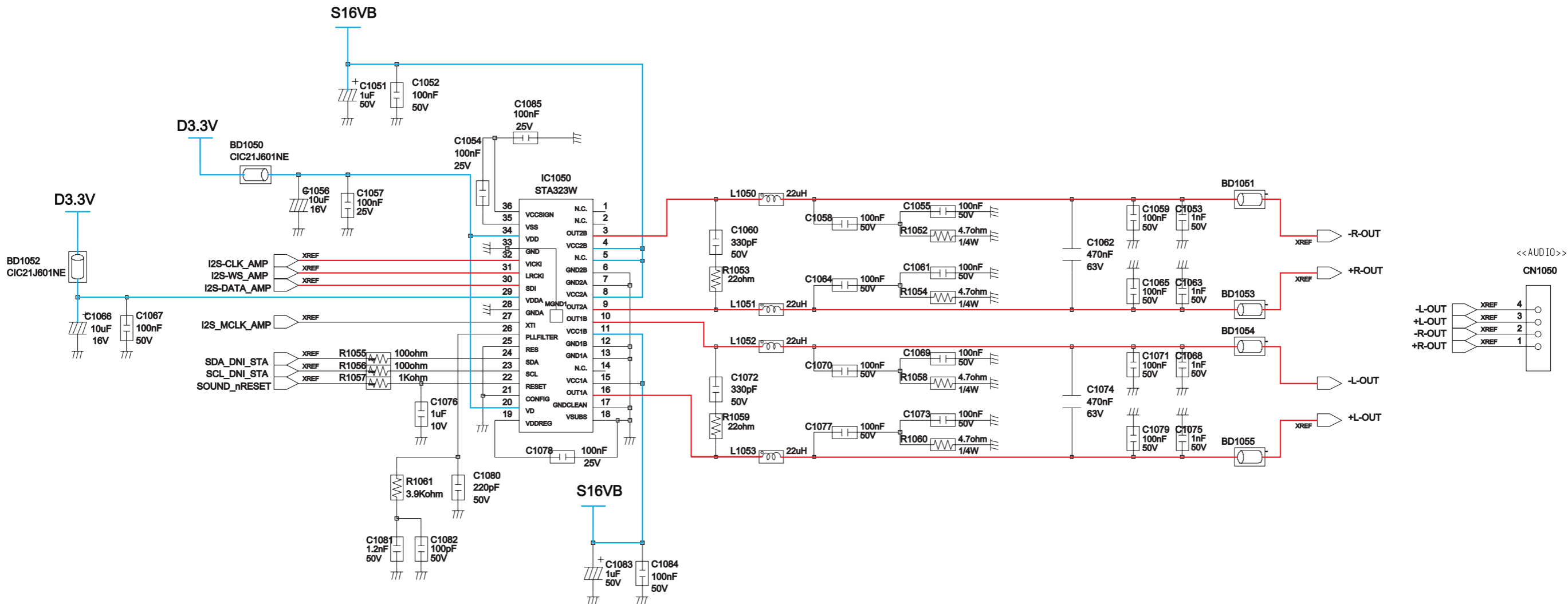
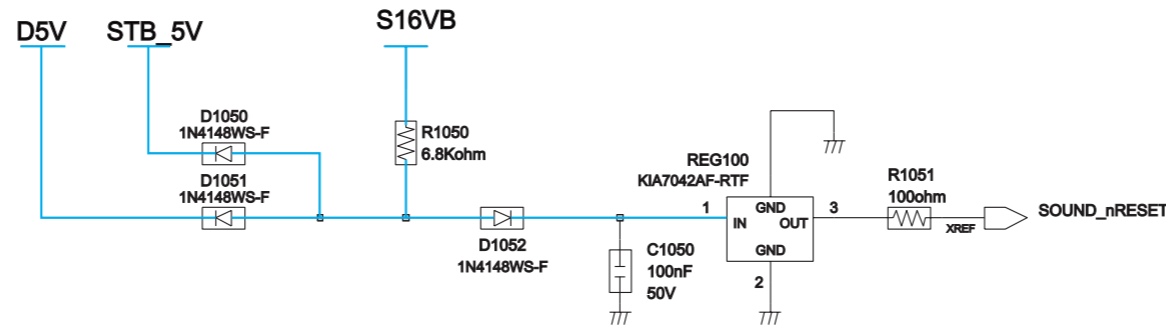
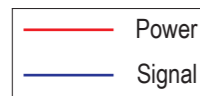
A	B	C	D
1. IR	1. 5VA	1. GND	1. LED1
2. GND	2. KEY-PWR	2. KEY1	2. KEY1
3. 5VA	3. GND	3. KEY2	3. KEY2
	4. LED1	4. GND	4. SDA-M1
	5. LED2		5. SCL-M1
	6. LED3		6. 5VB
			7. GND

Used as follows...

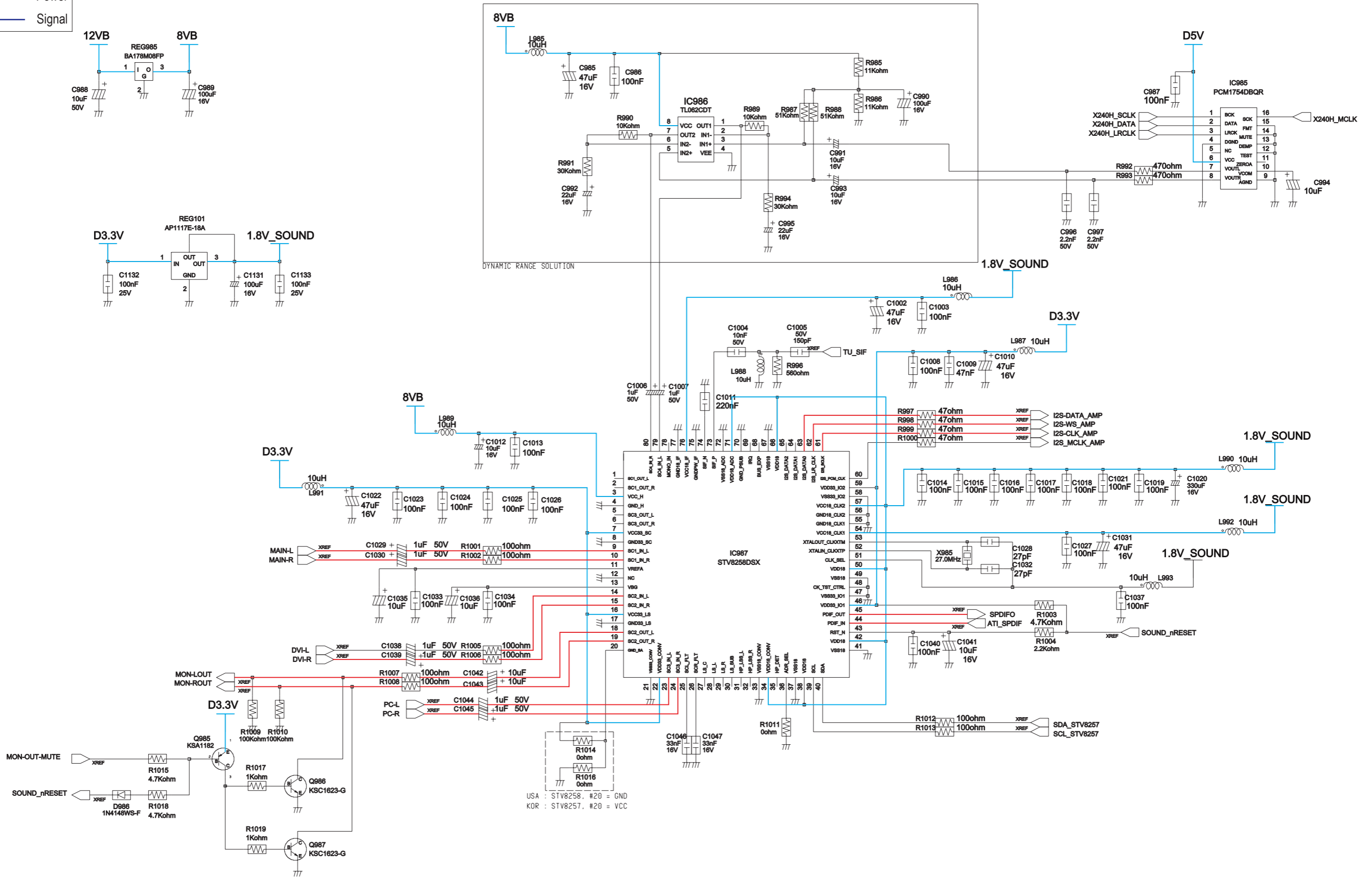
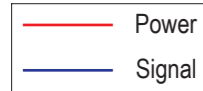
L3 : A+B, C
L5 : A, B, C
L7 : A, D



10-1-4 Main-4

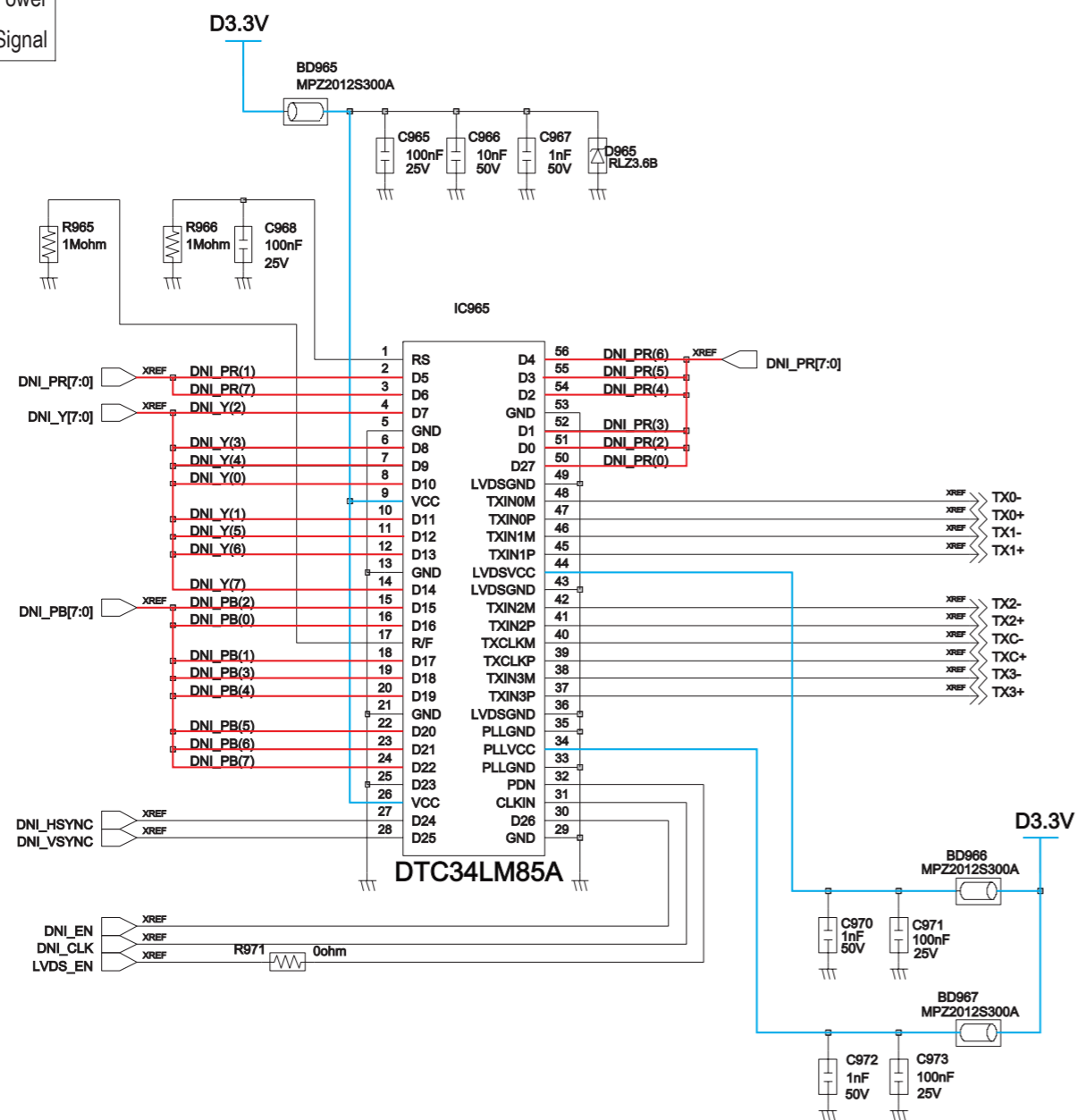
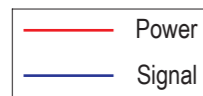


10-1-5 Main-5

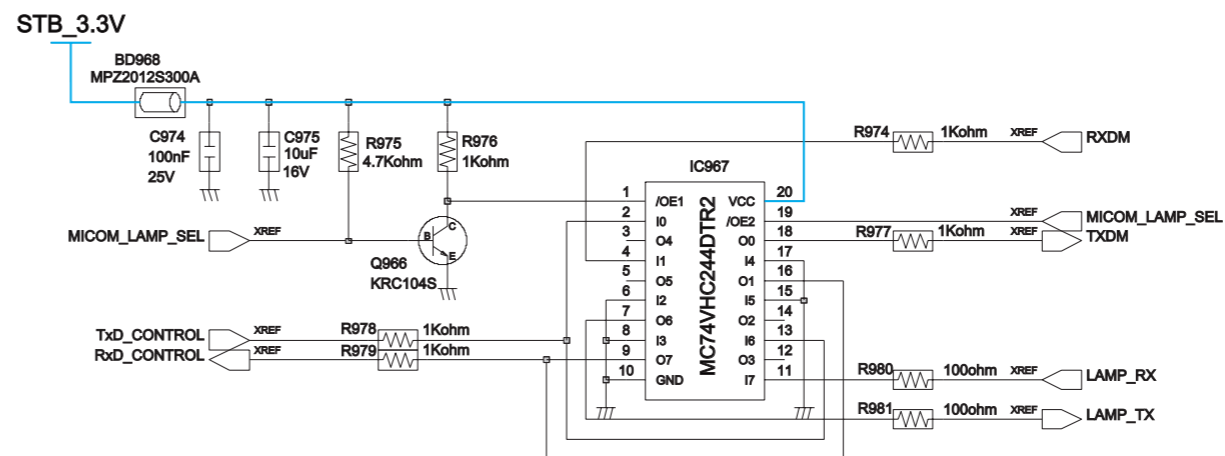
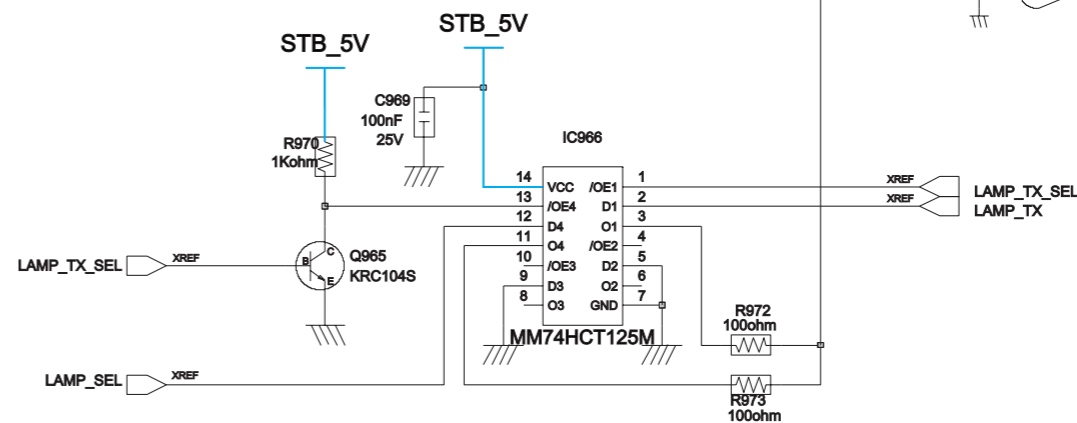
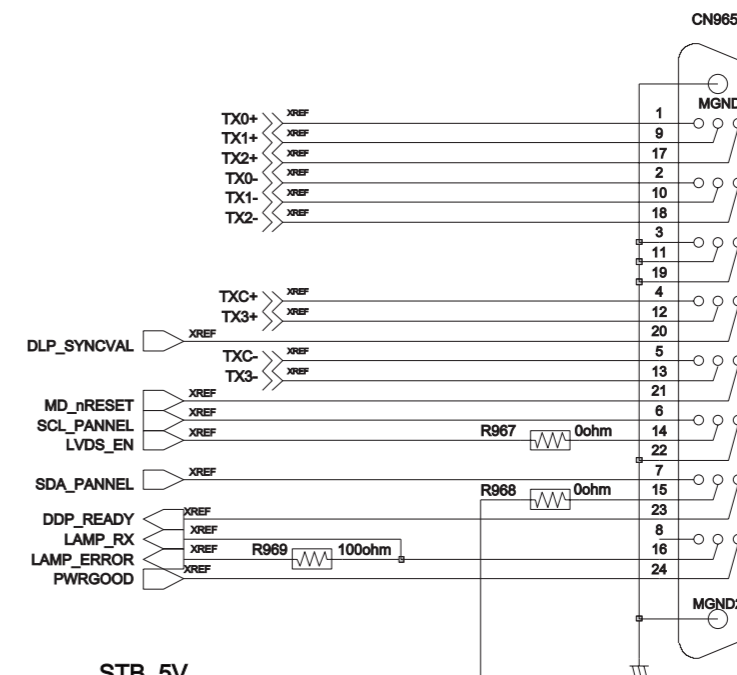


USA : STV8258, #20 = GND
KOR : STV8257, #20 = VCC

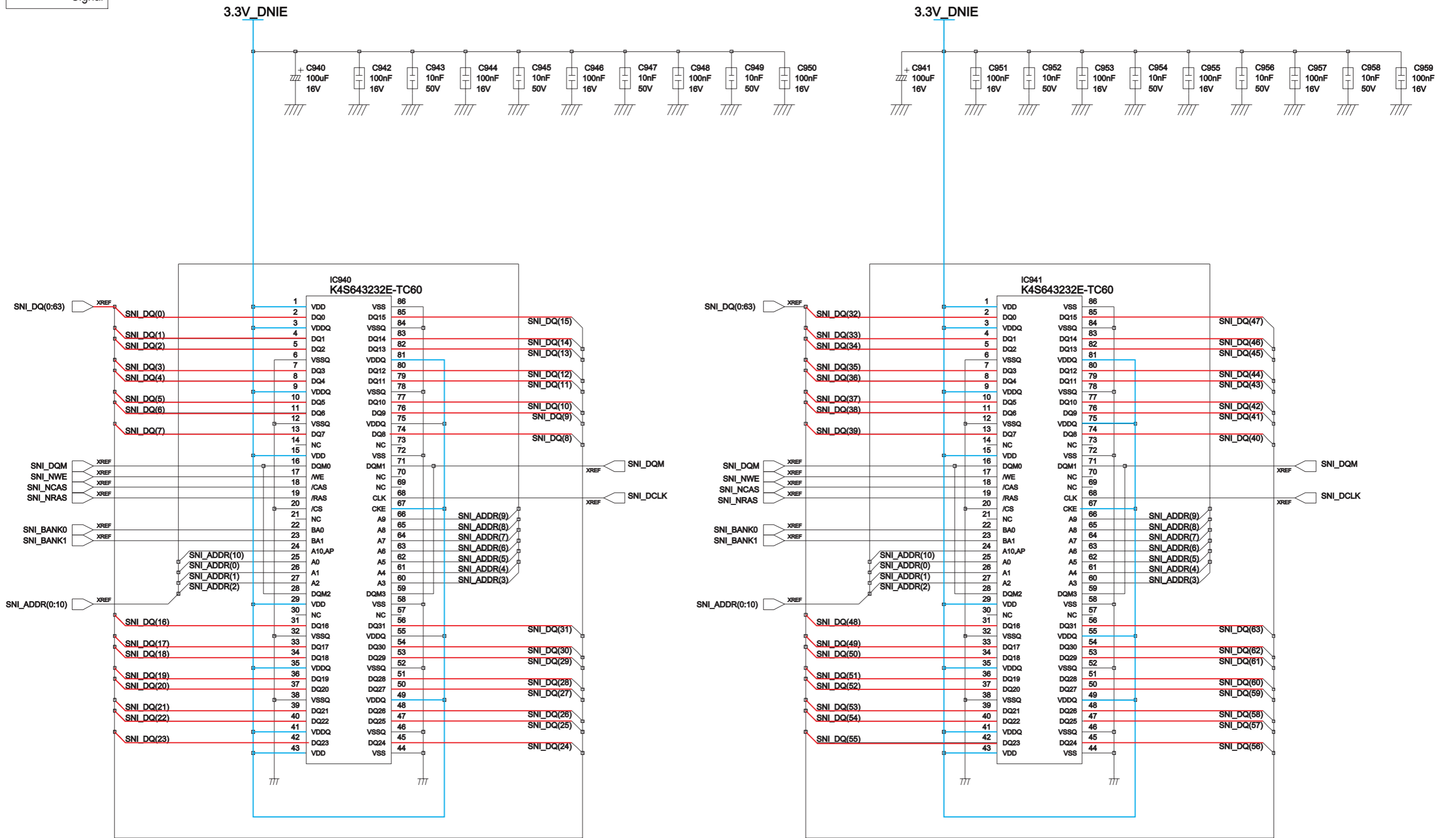
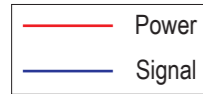
10-1-6 Main-6



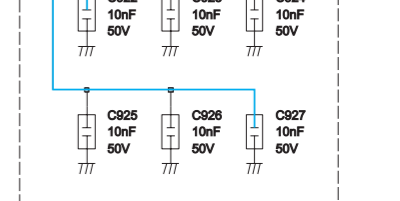
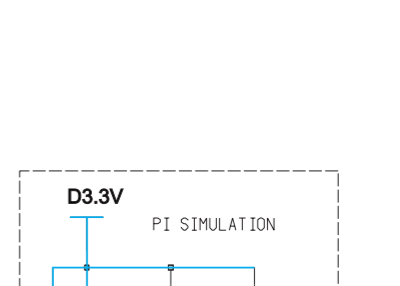
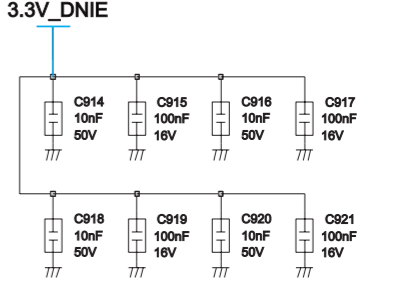
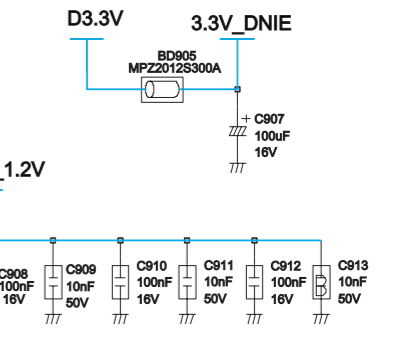
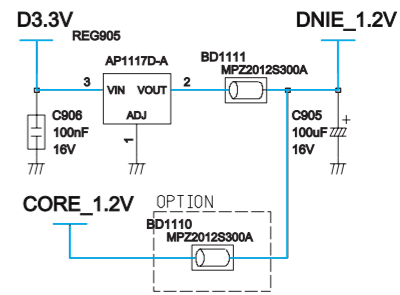
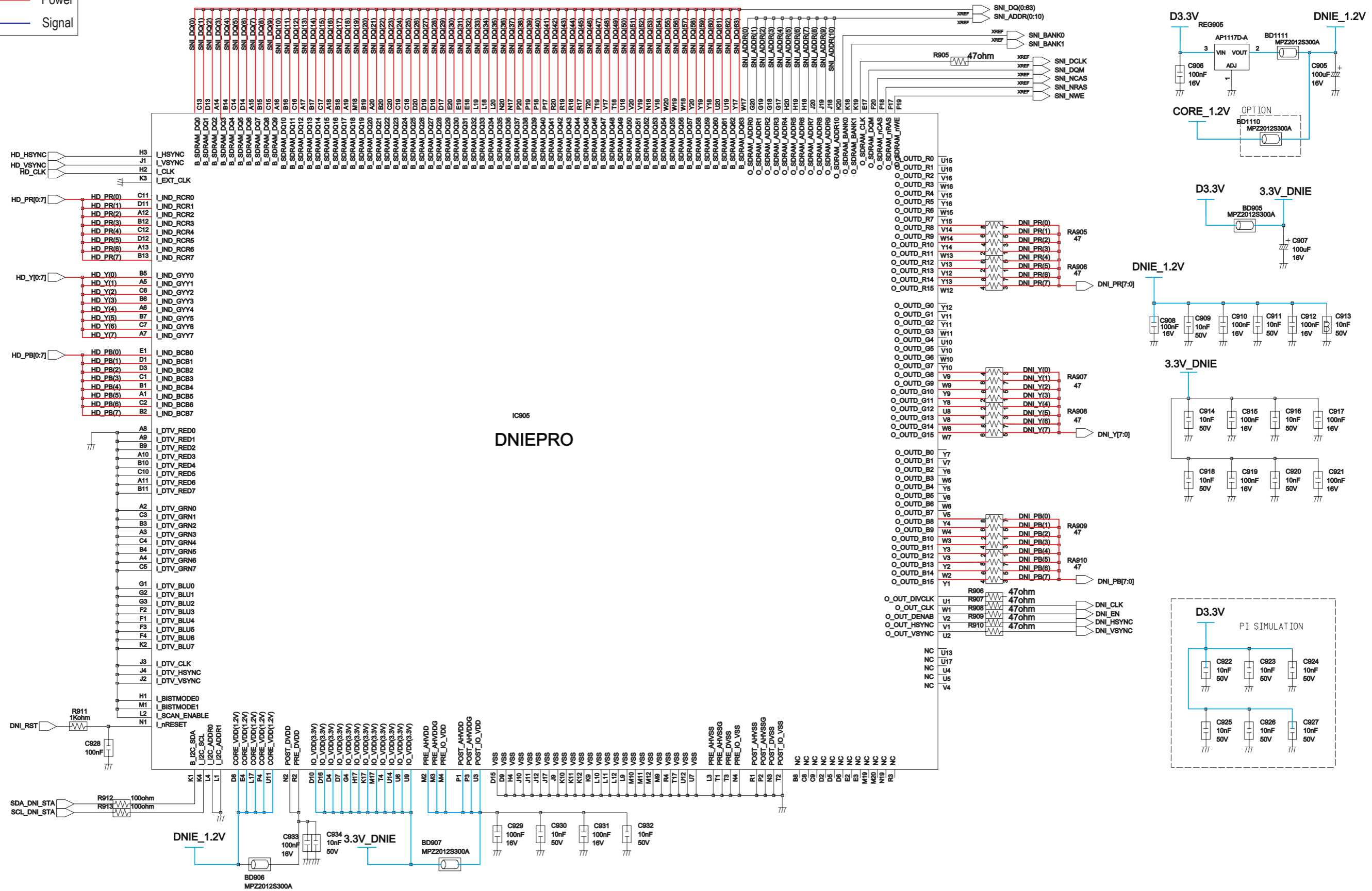
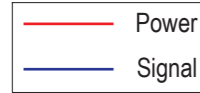
LVDS OUTPUT



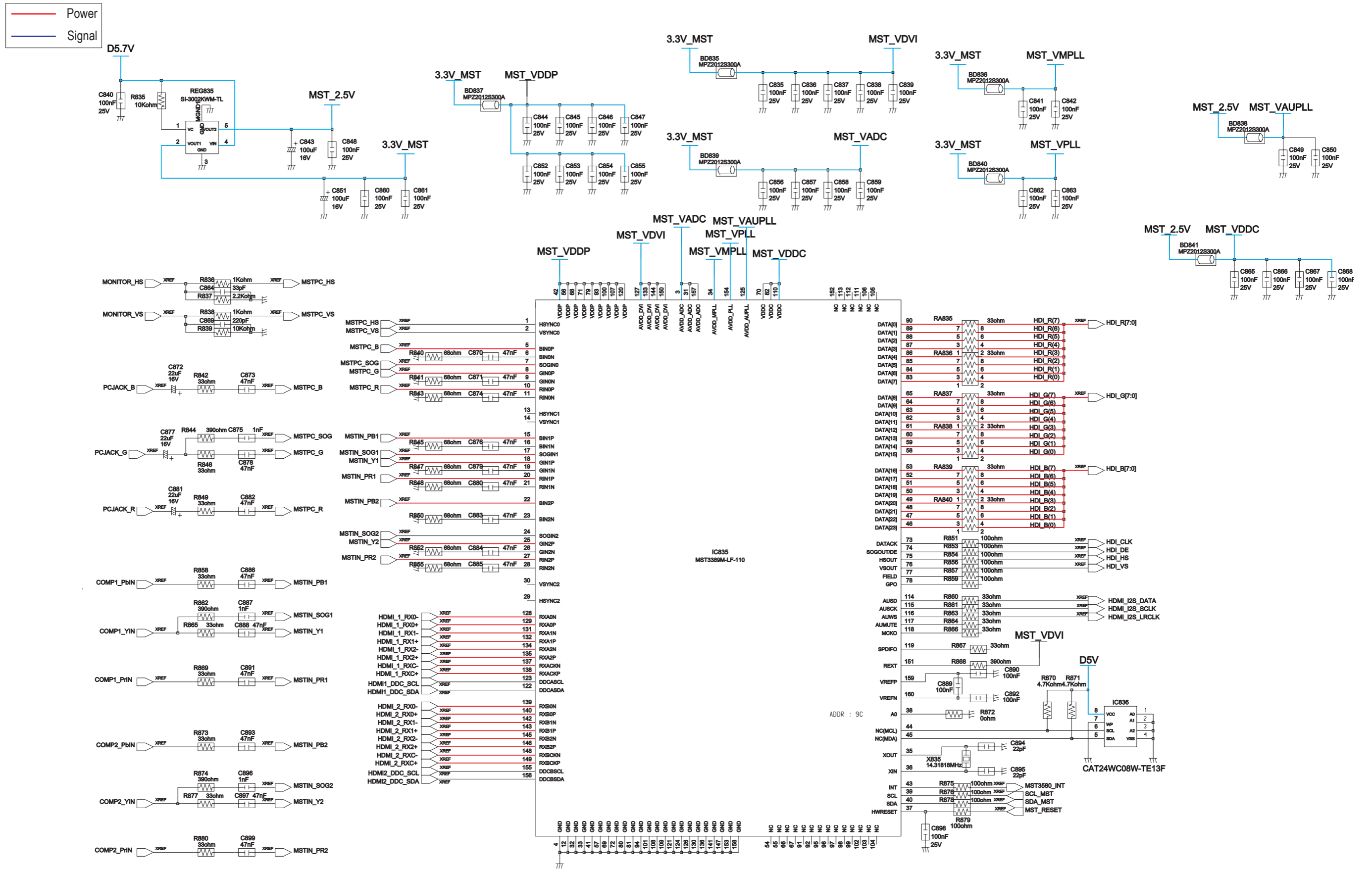
10-1-7 Main-7



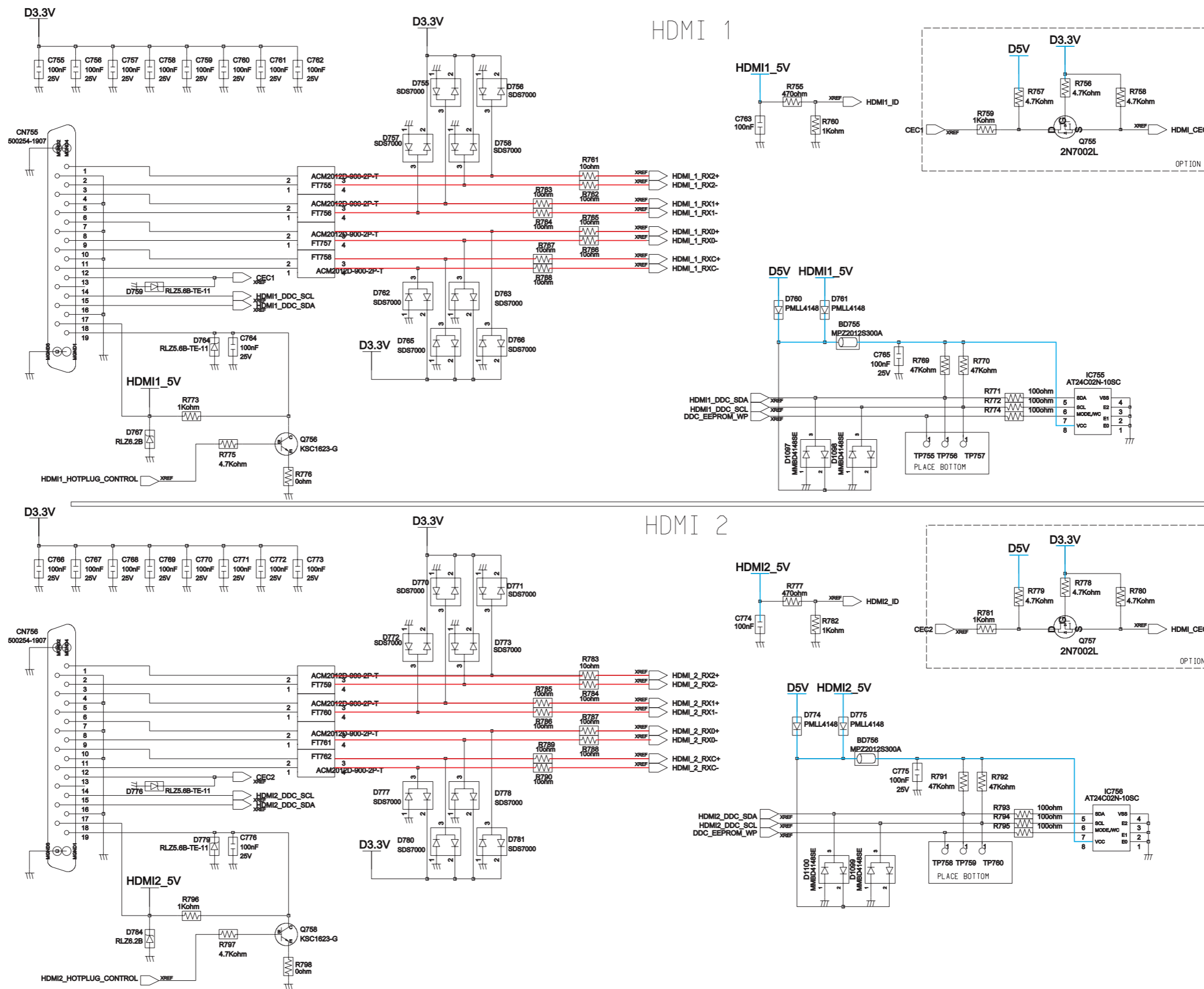
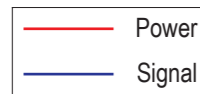
10-1-8 Main-8



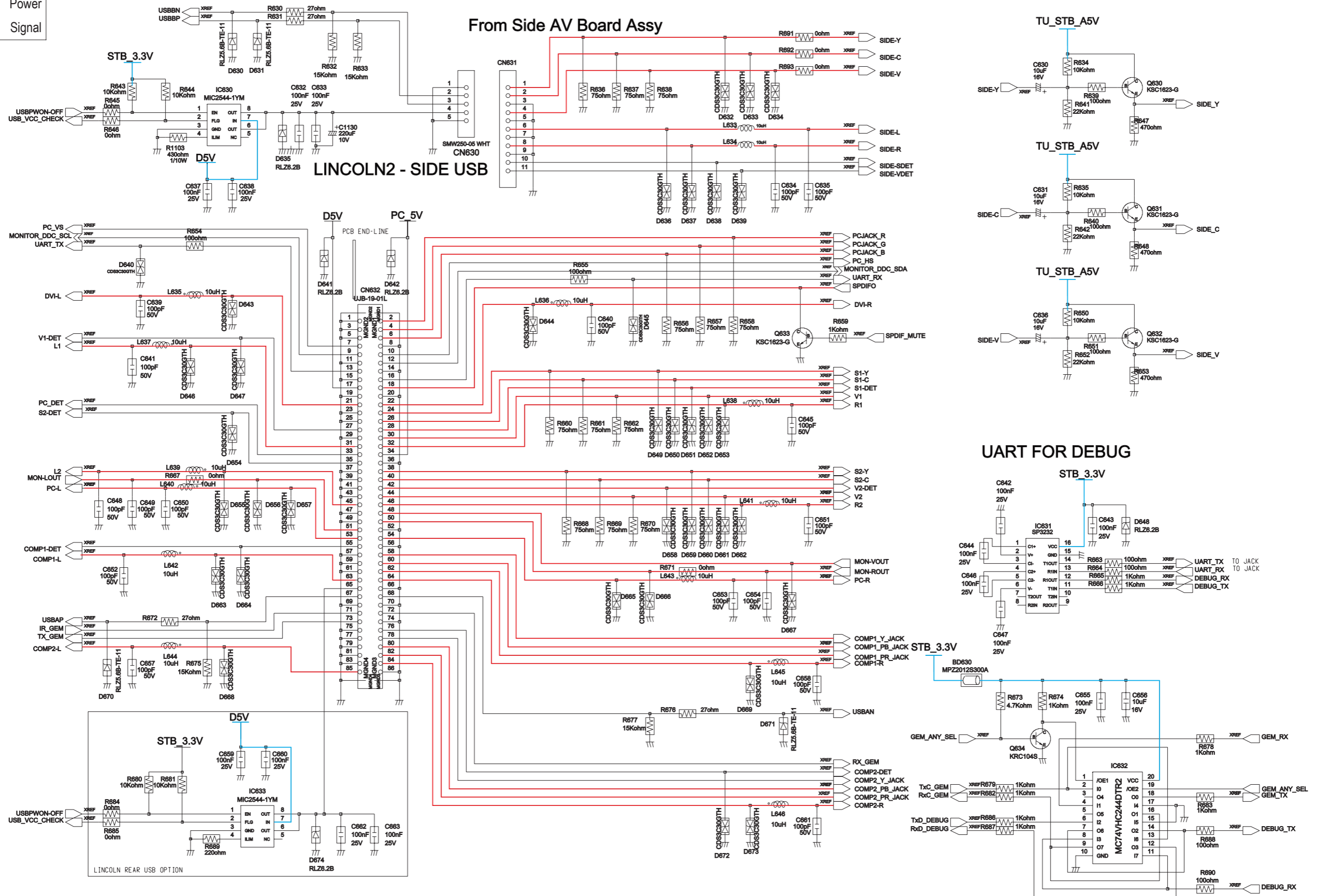
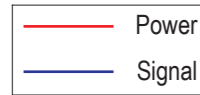
10-1-9 Main-9



10-1-10 Main-10



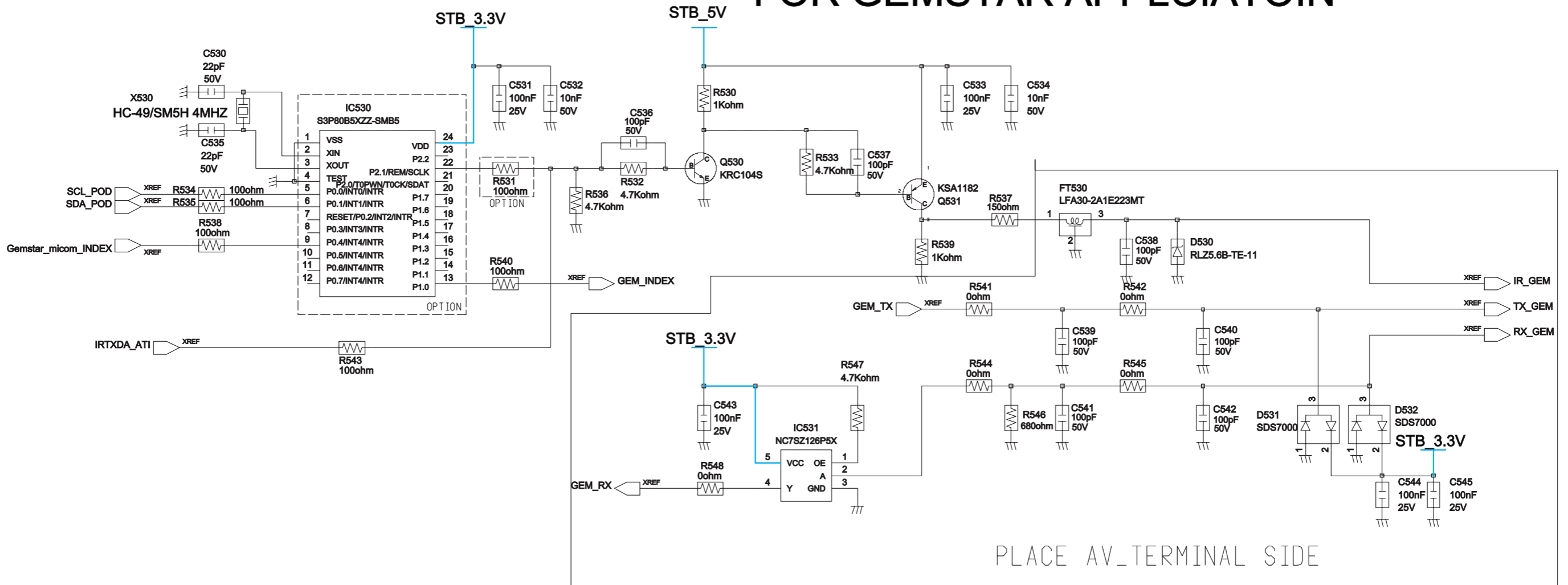
10-1-12 Main-12



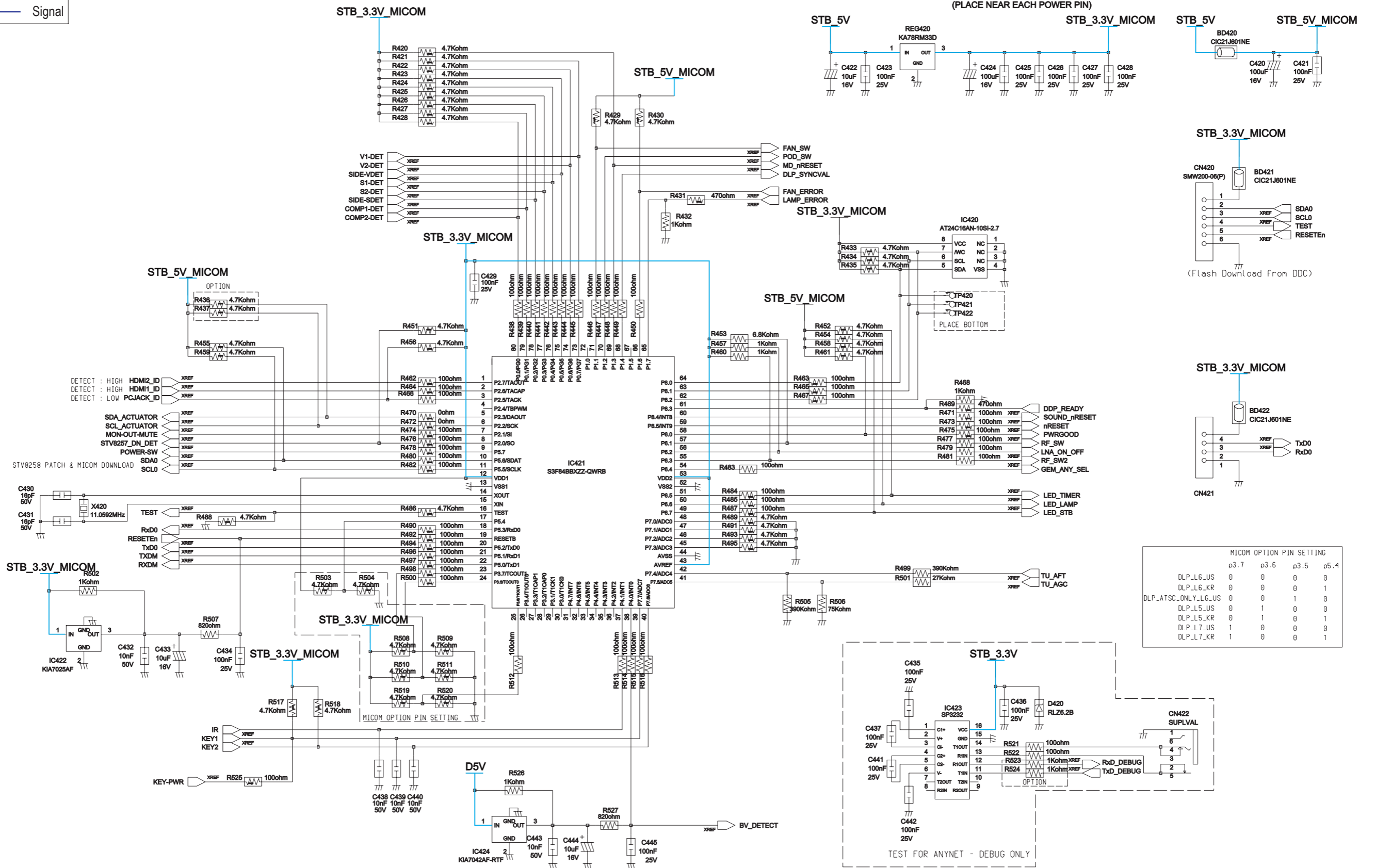
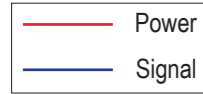
10-1-14 Main-14



FOR GEMSTAR APPLCIATOIN



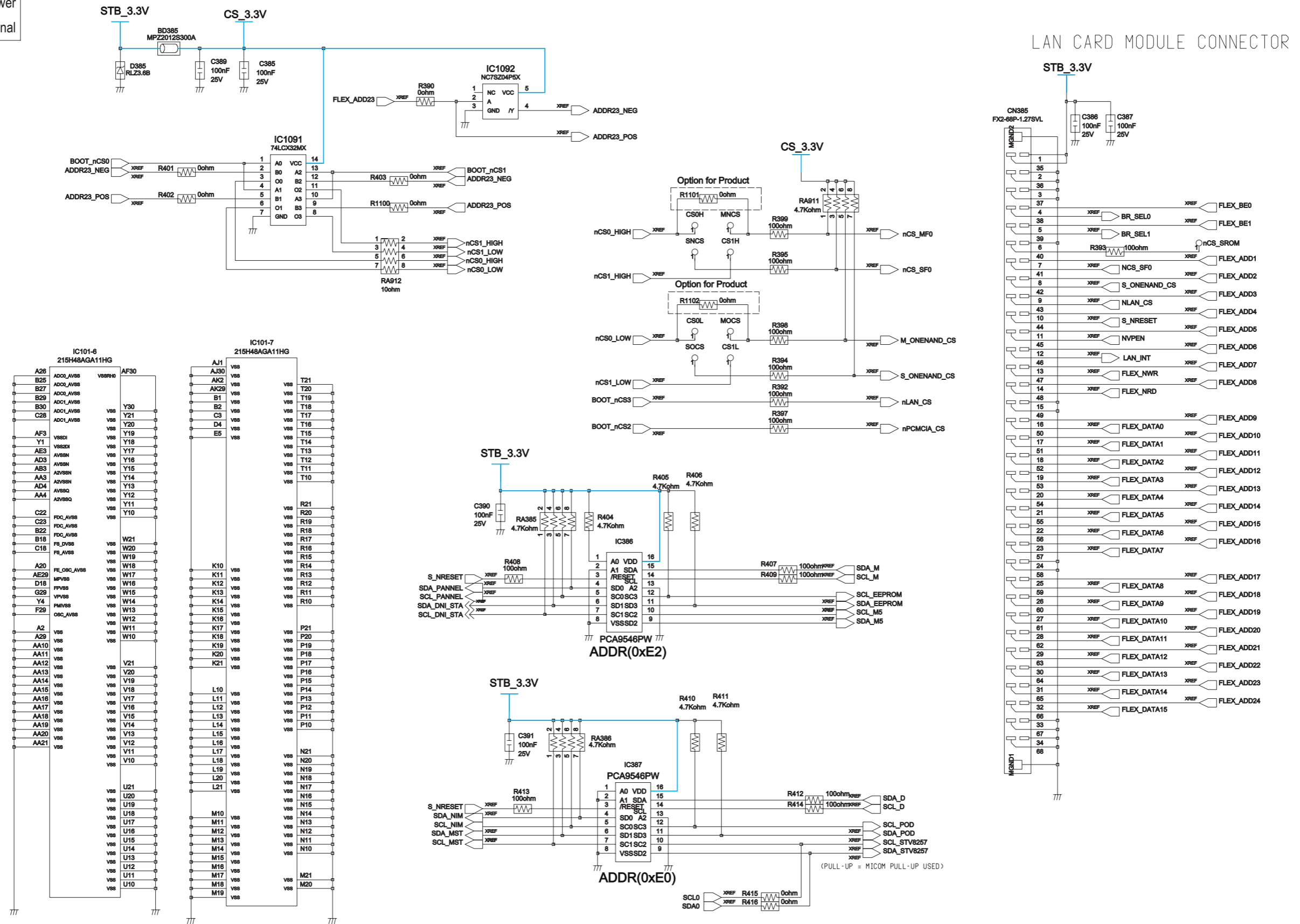
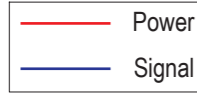
10-1-15 Main-15



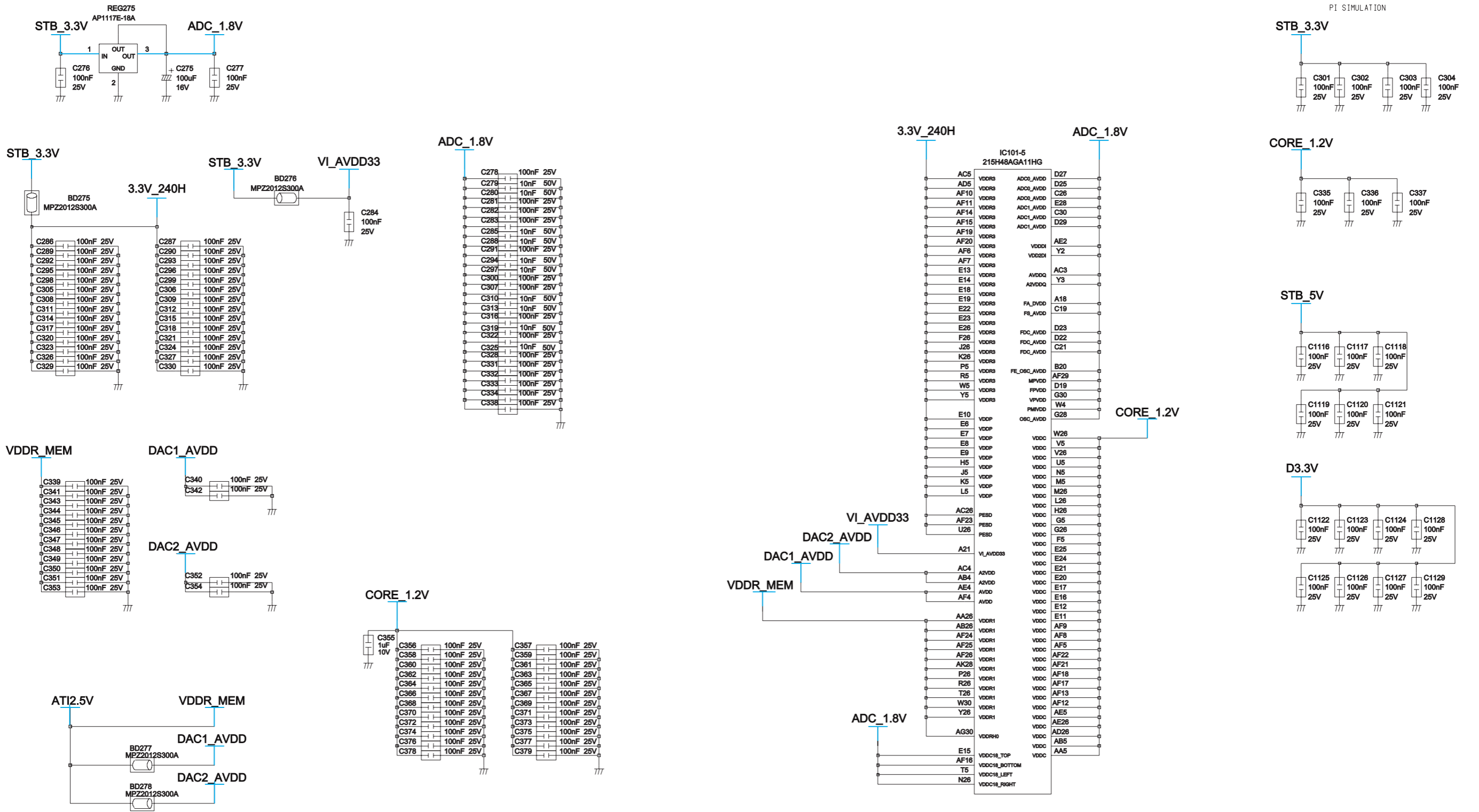
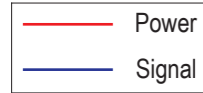
MICOM OPTION PIN SETTING

	p3.7	p3.6	p3.5	p5.4
DLP_L6_US	0	0	0	0
DLP_L6_KR	0	0	0	1
DLP_ATSC_ONLY_L6_US	0	0	1	0
DLP_L5_US	0	1	0	0
DLP_L5_KR	0	1	0	1
DLP_L7_US	1	0	0	0
DLP_L7_KR	1	0	0	1

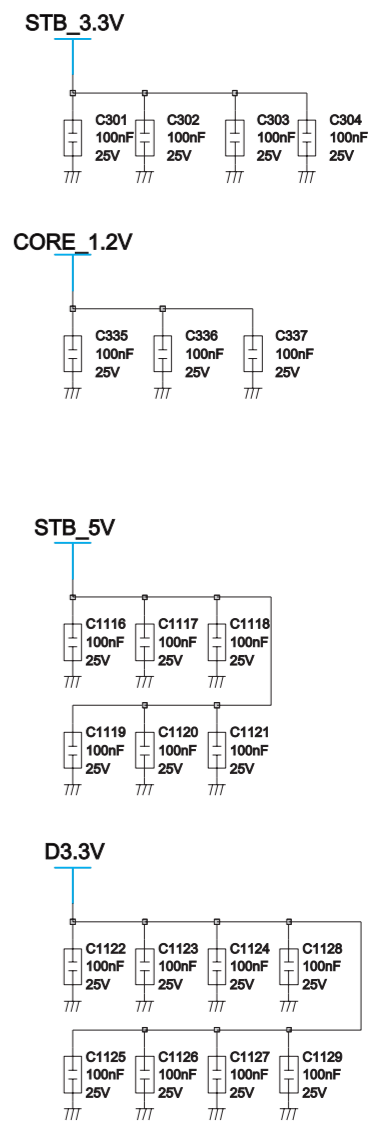
10-1-16 Main-16



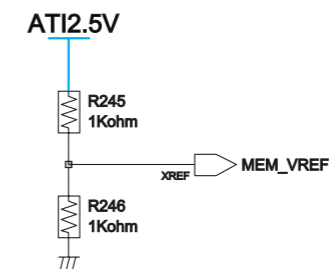
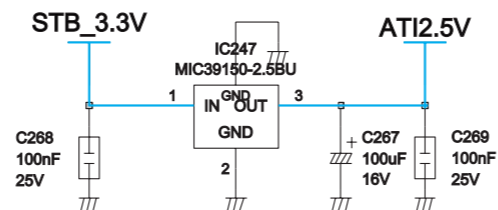
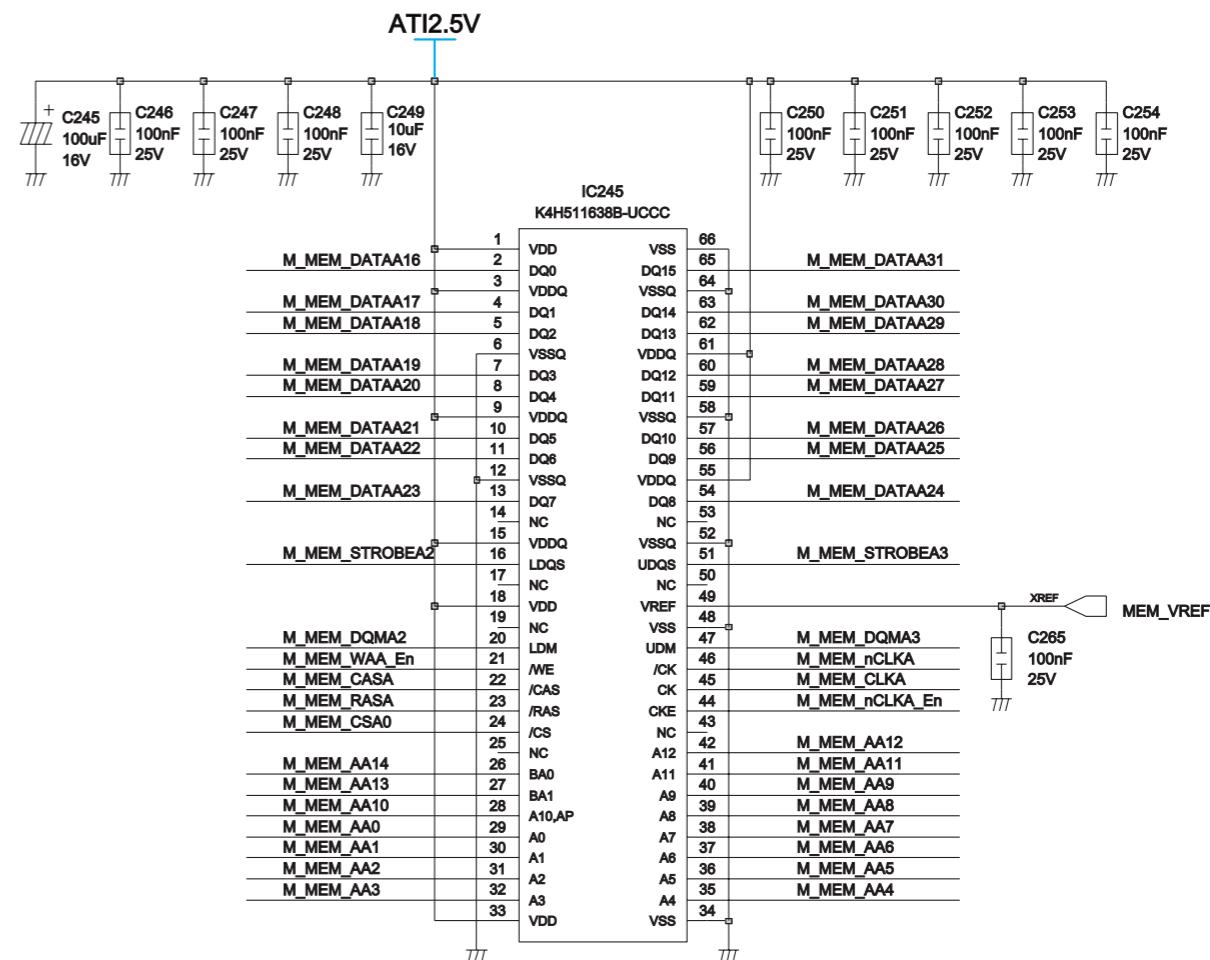
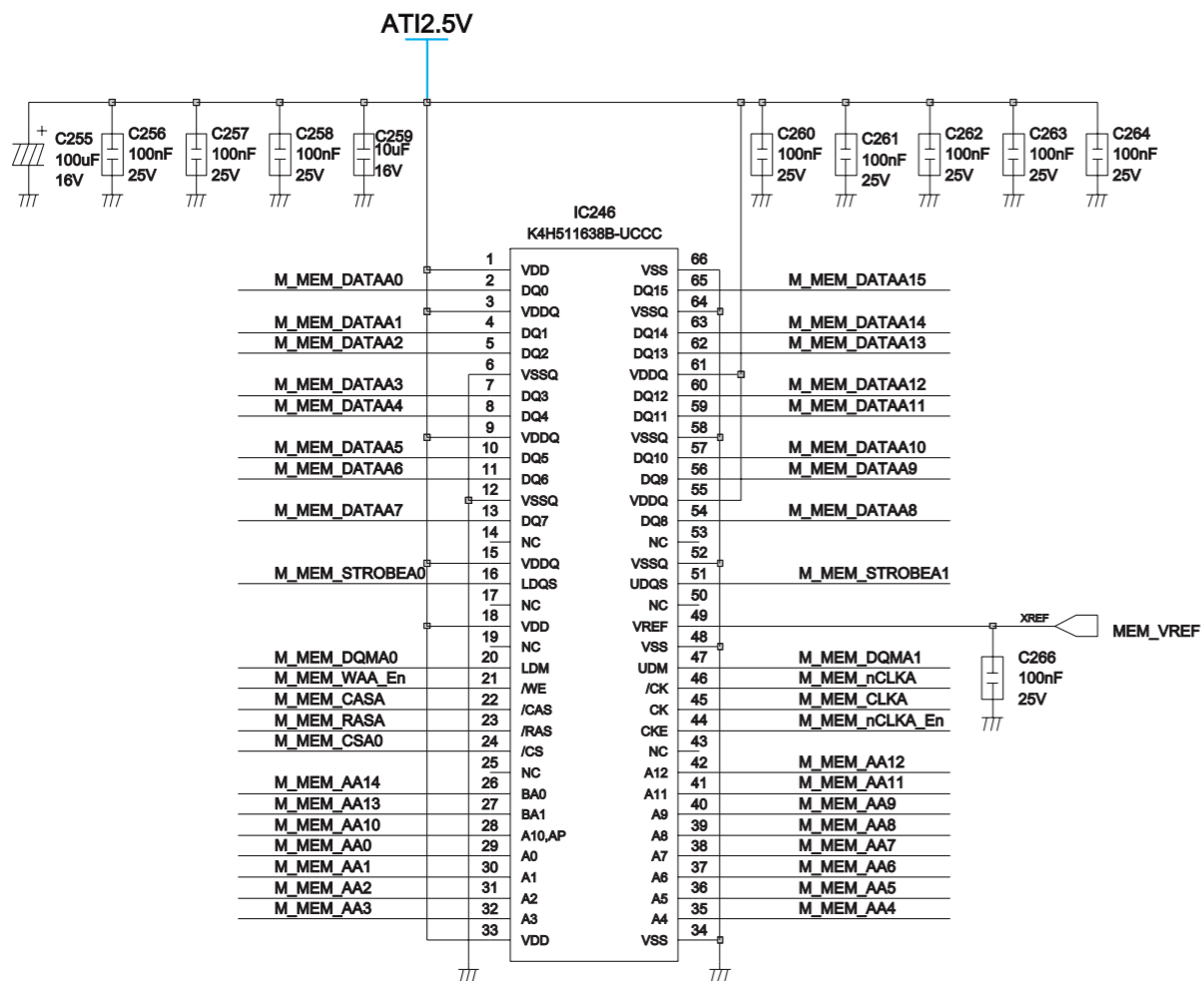
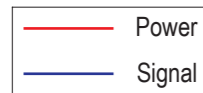
10-1-17 Main-17



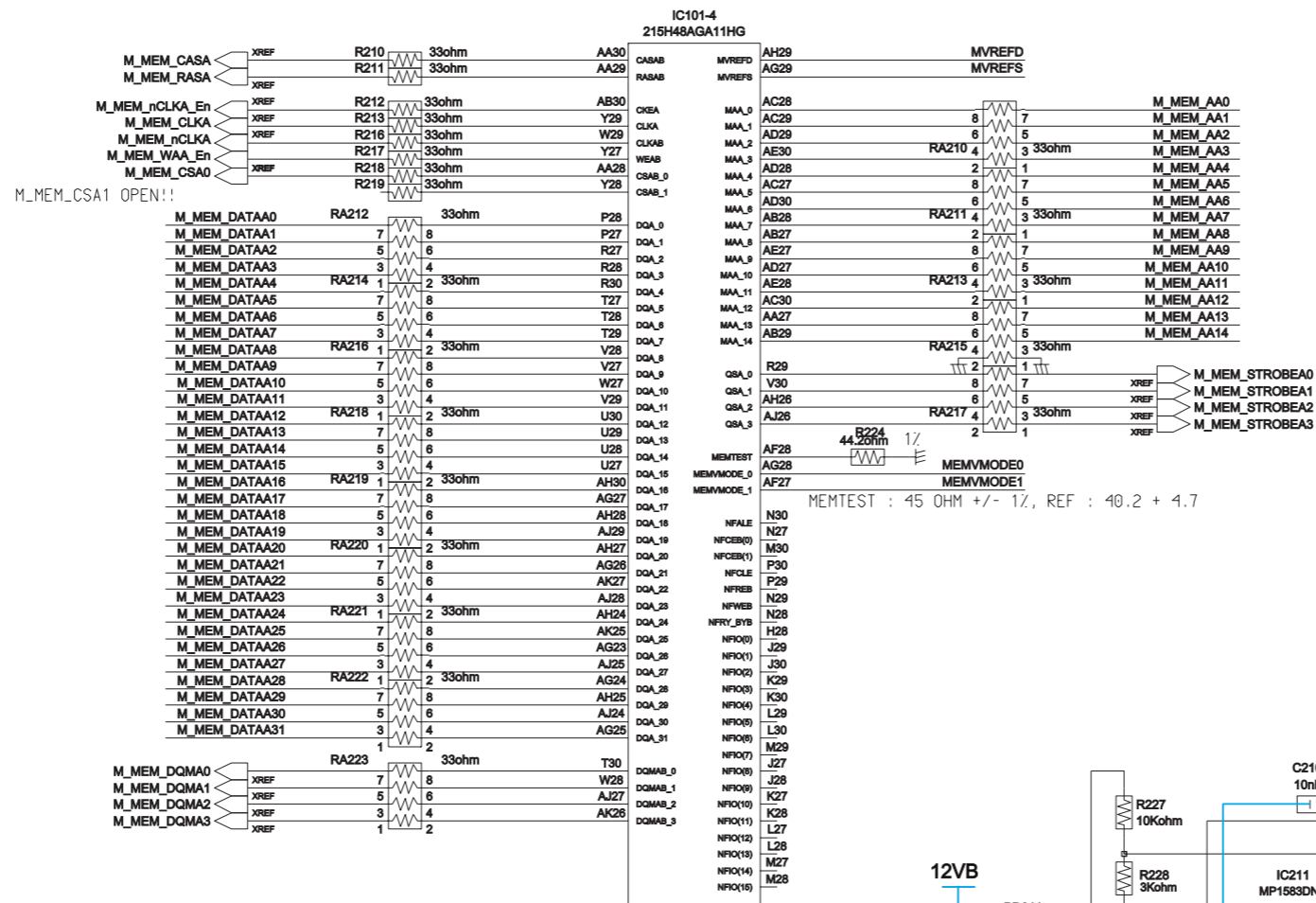
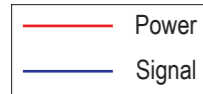
PI SIMULATION



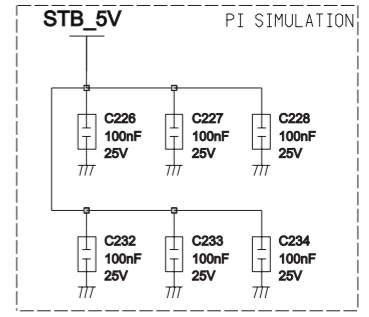
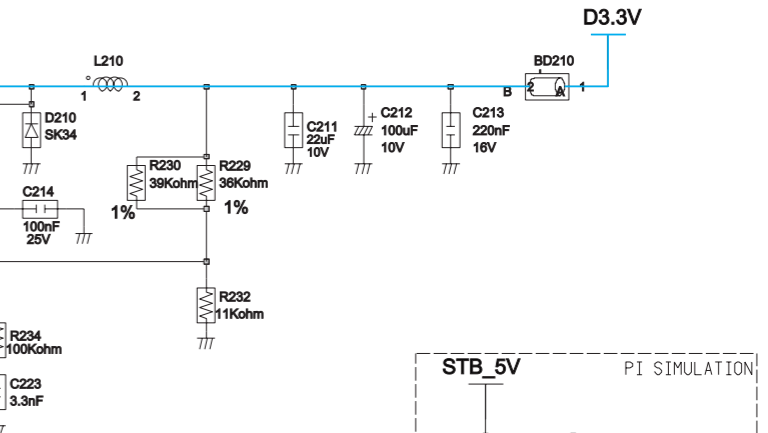
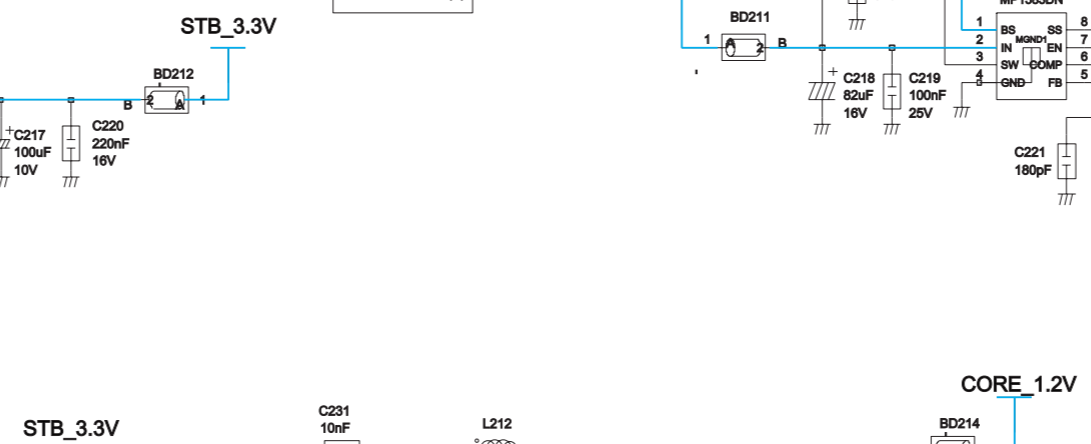
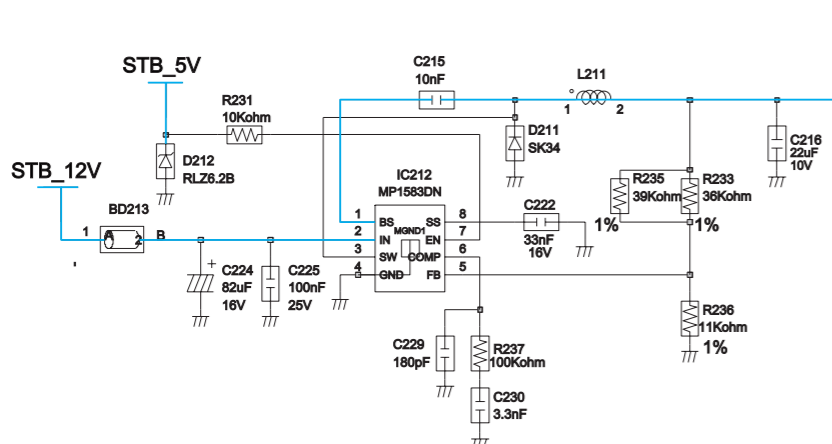
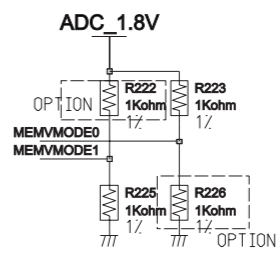
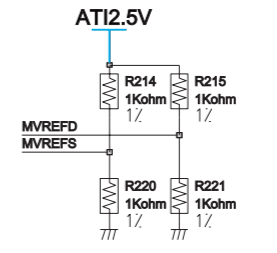
10-1-18 Main-18



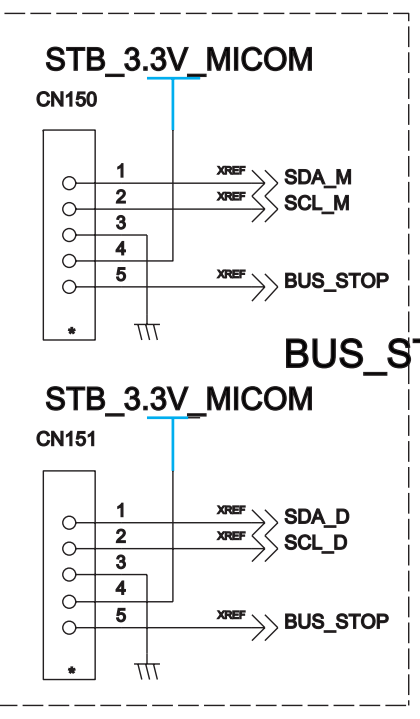
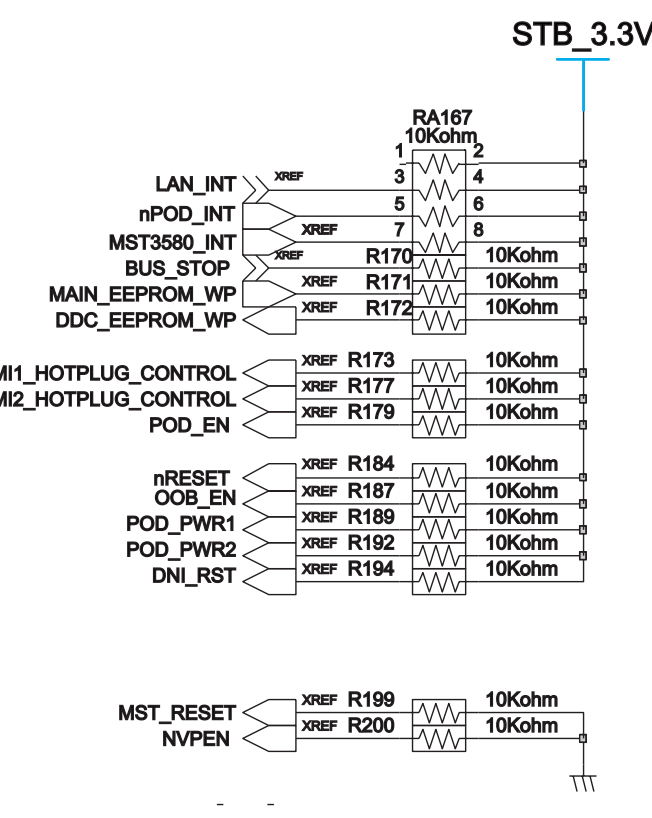
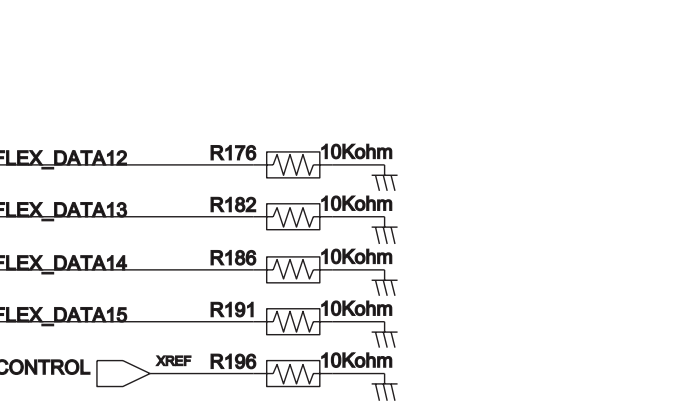
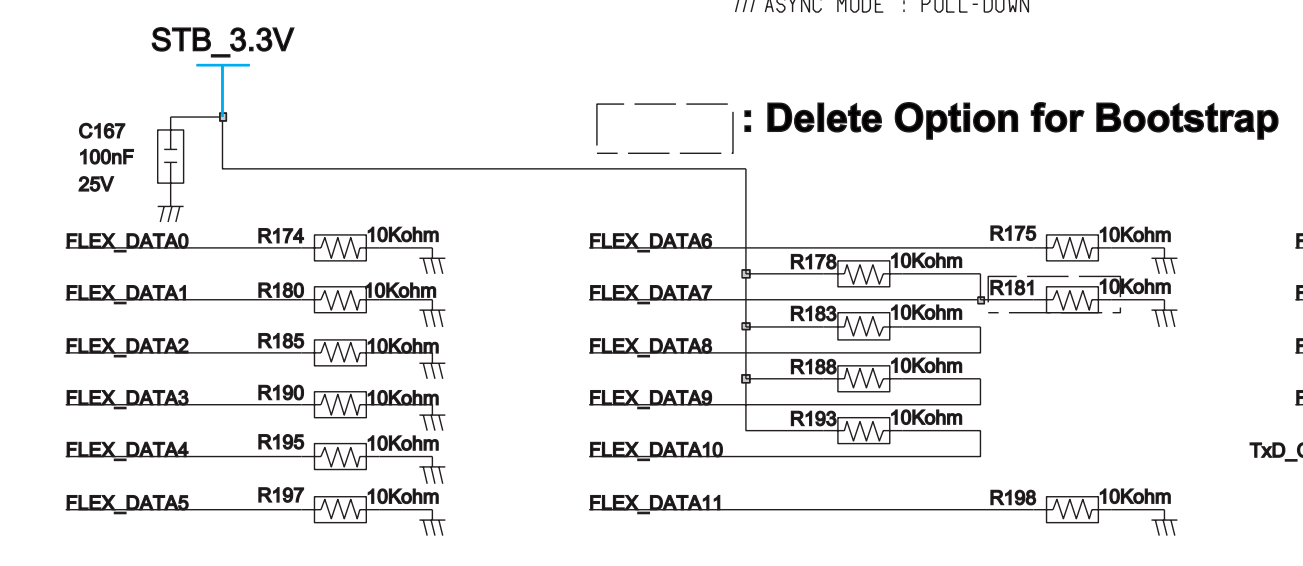
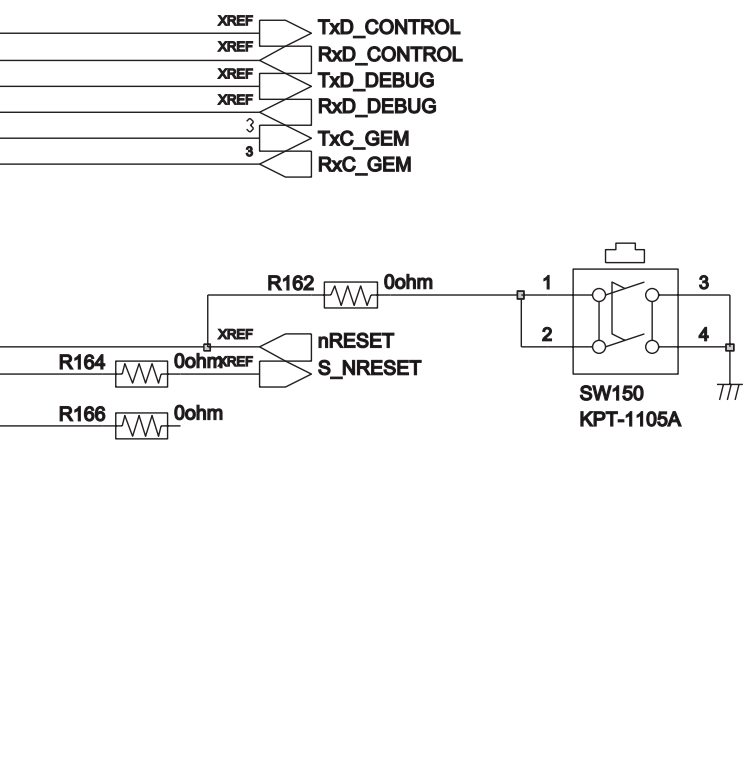
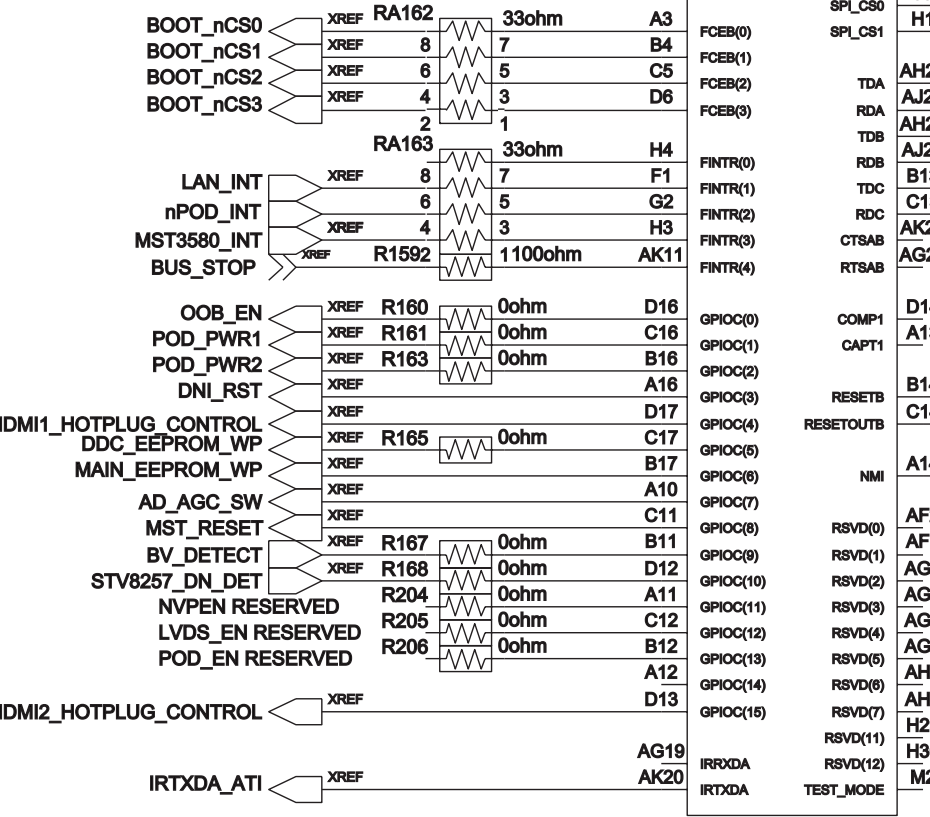
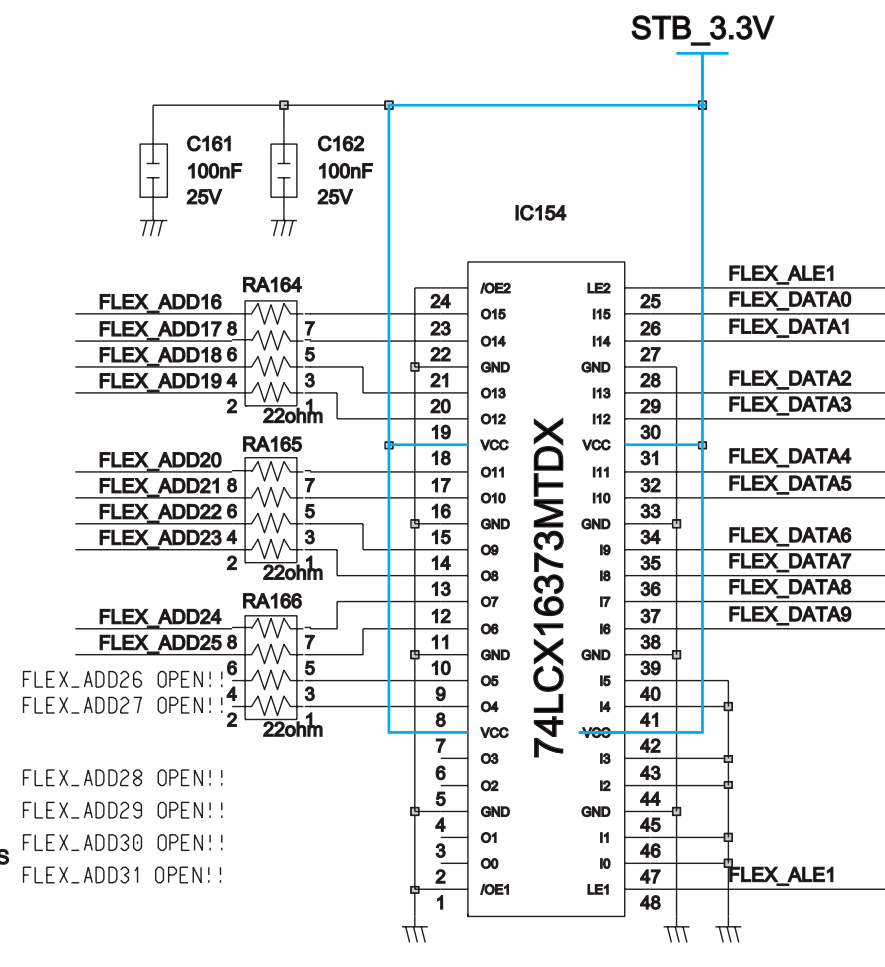
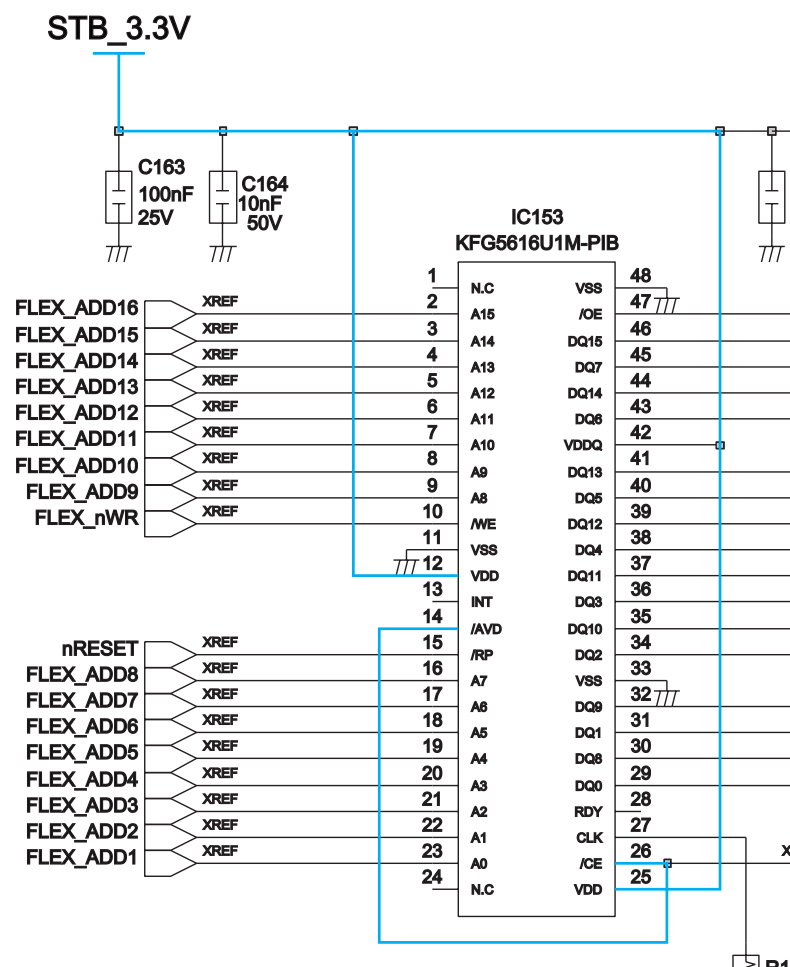
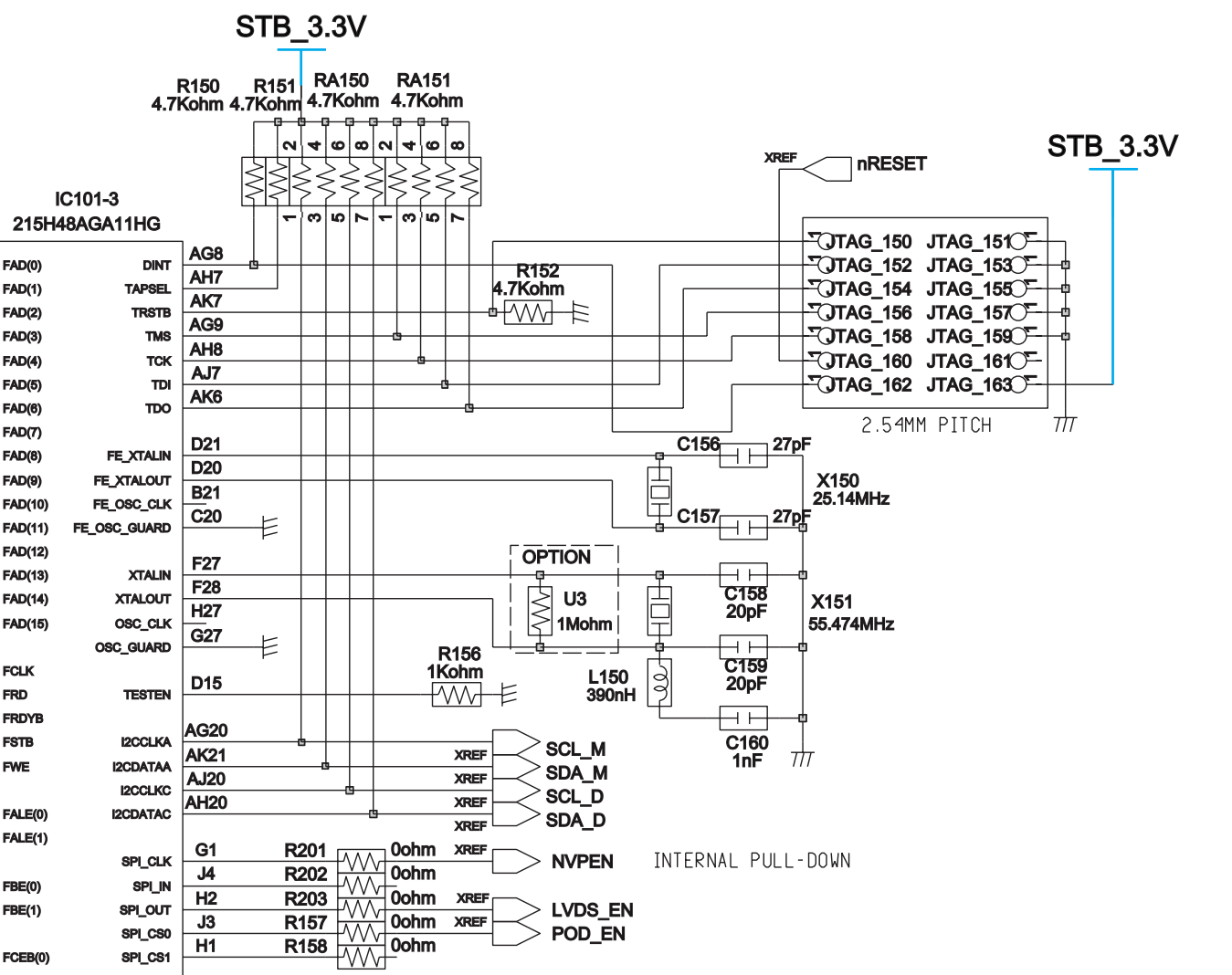
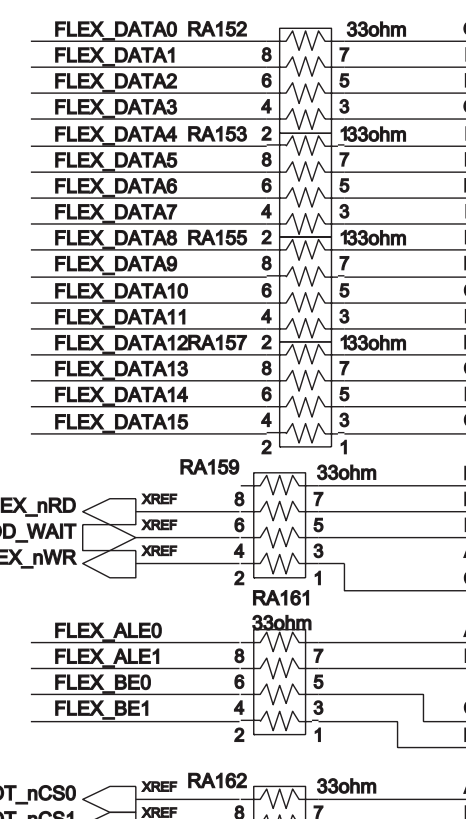
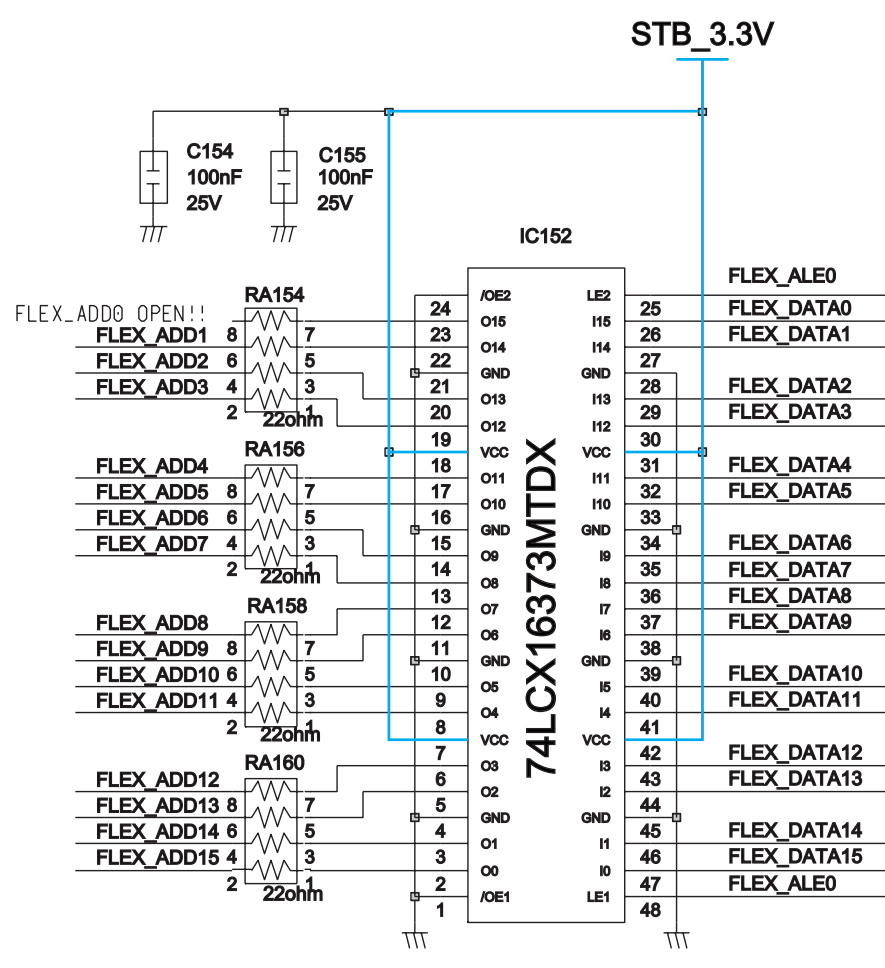
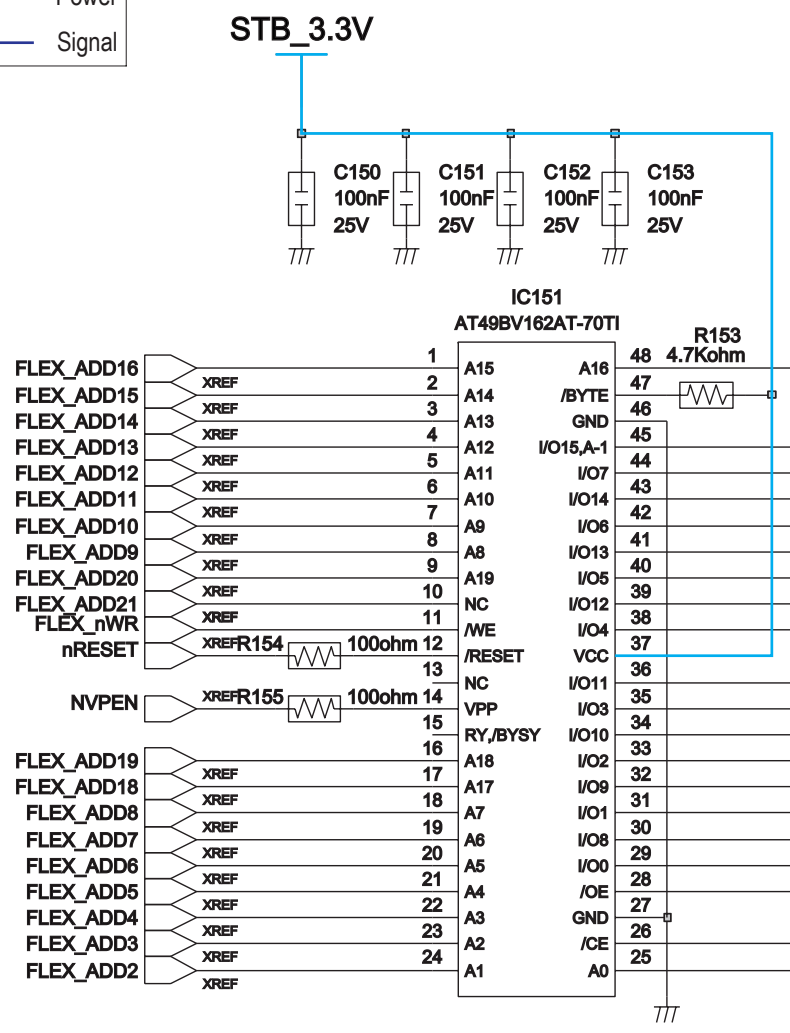
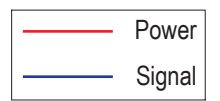
10-1-19 Main-19



MEMTEST : 45 OHM +/- 1%, REF : 40.2 + 4.7



10-1-20 Main-20



BUS_STOP for Debug

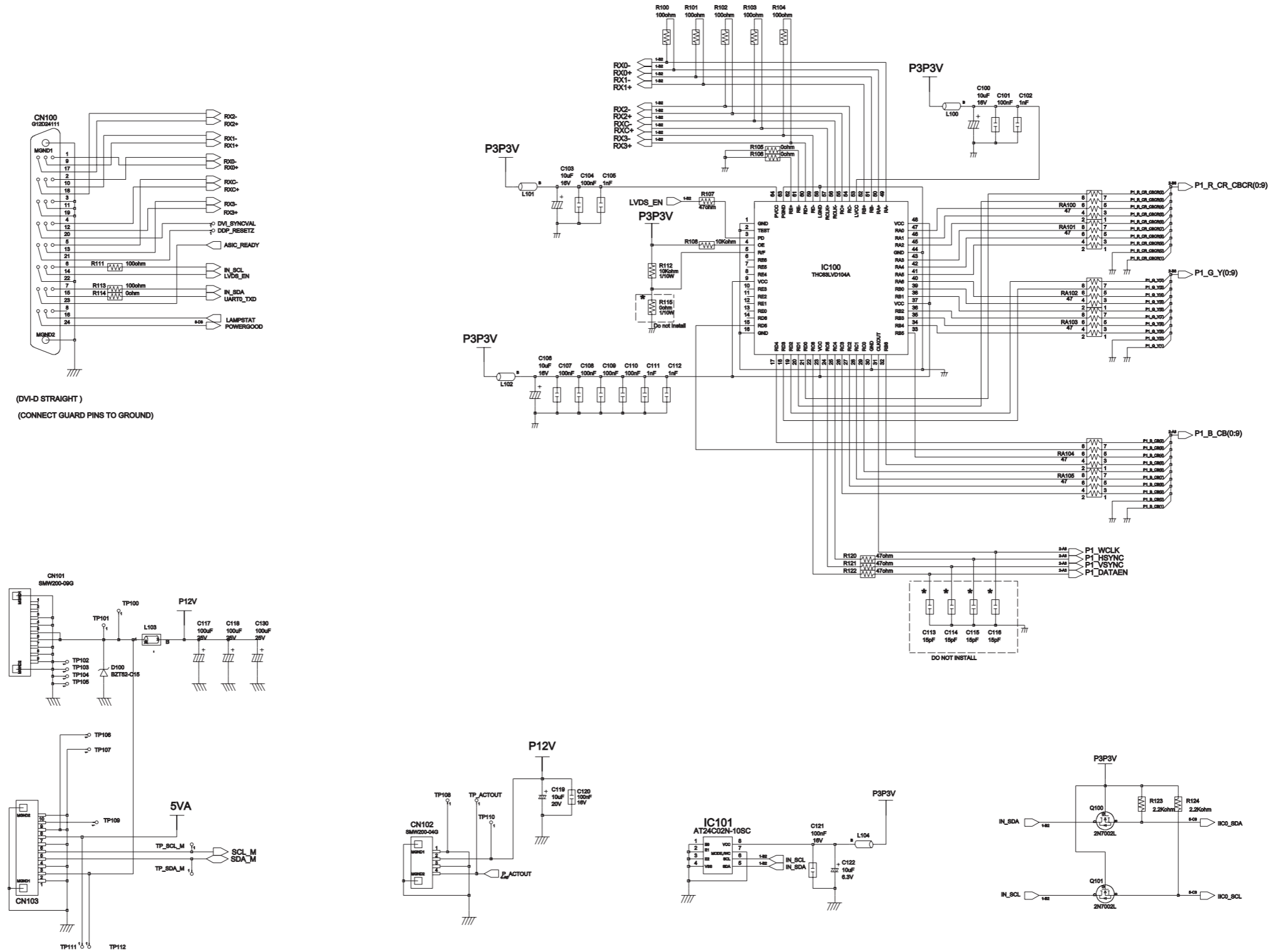
/// ASYNC MODE : PULL-DOWN

: Delete Option for Bootstrap

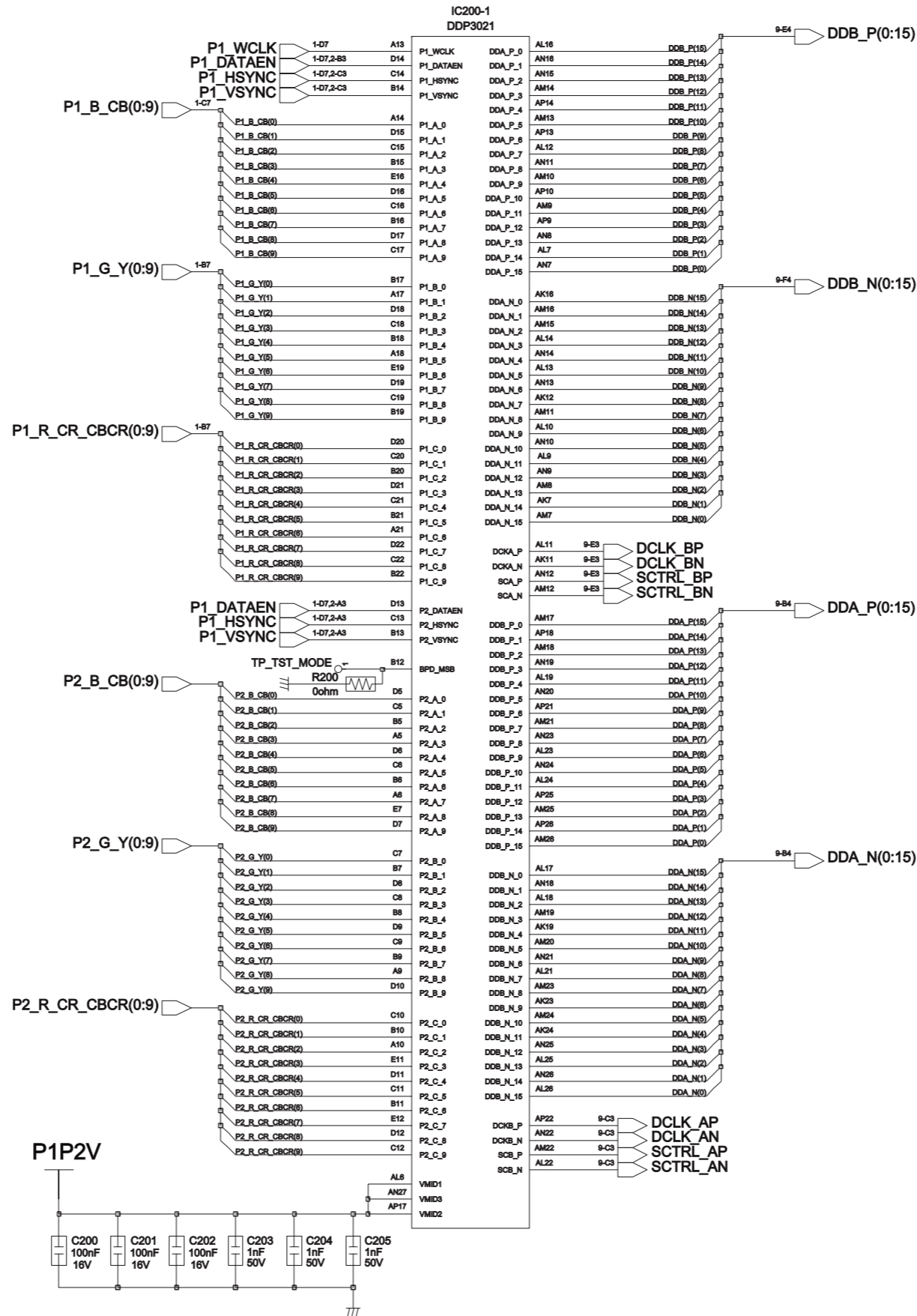
10-2 DMD Board

10-2-1 DMD Board-1

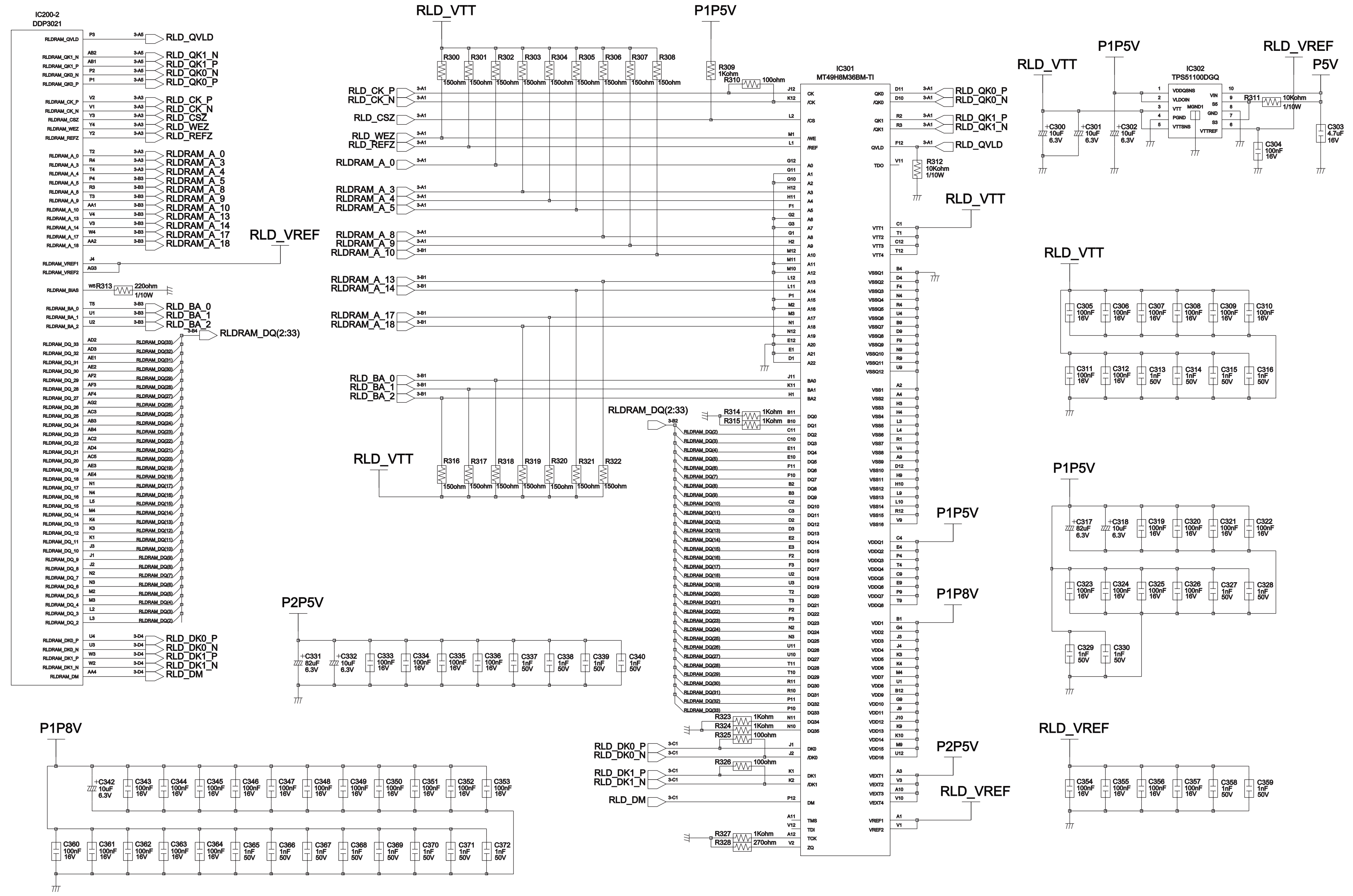
INPUT



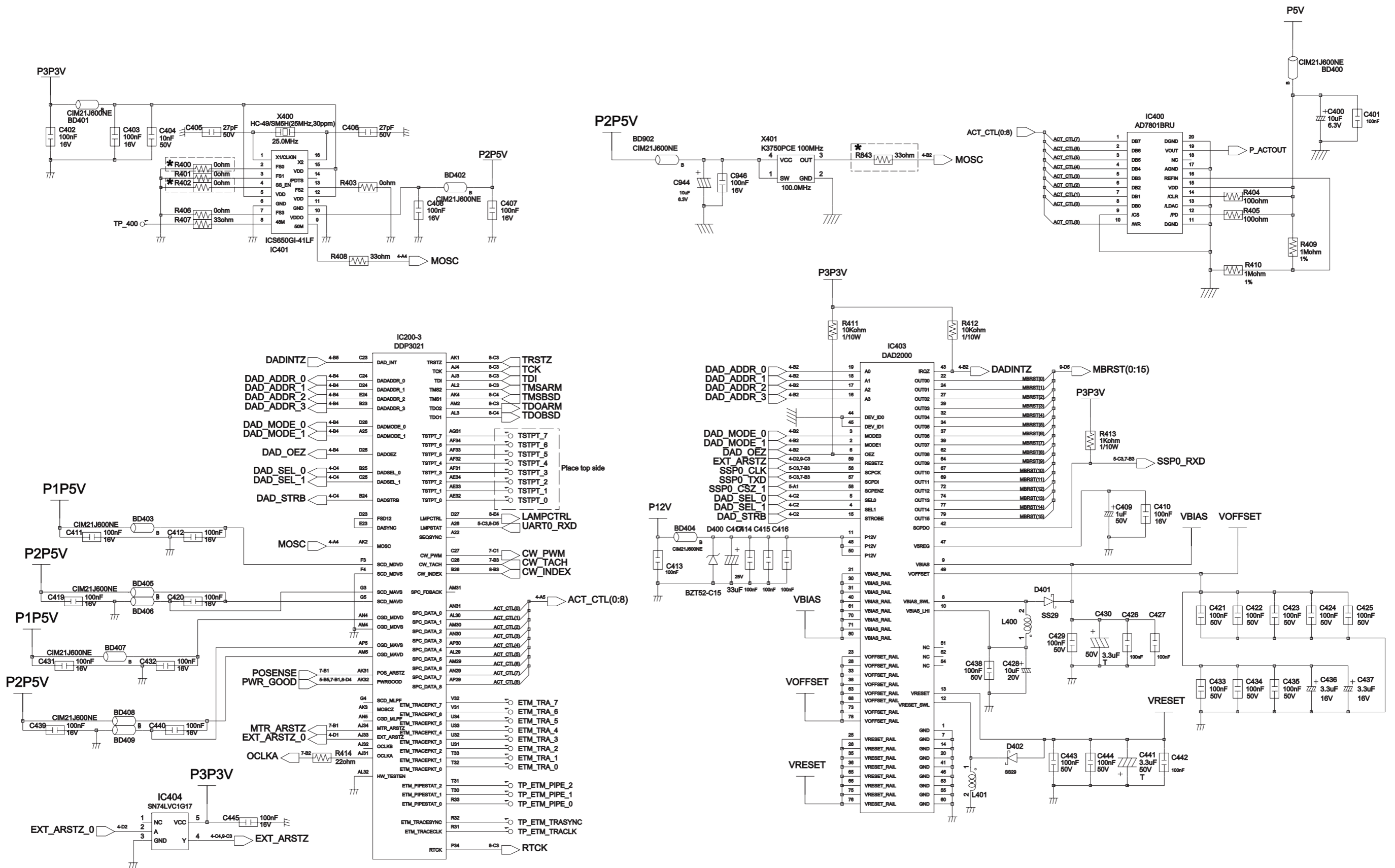
10-2-2 DMD Board-2



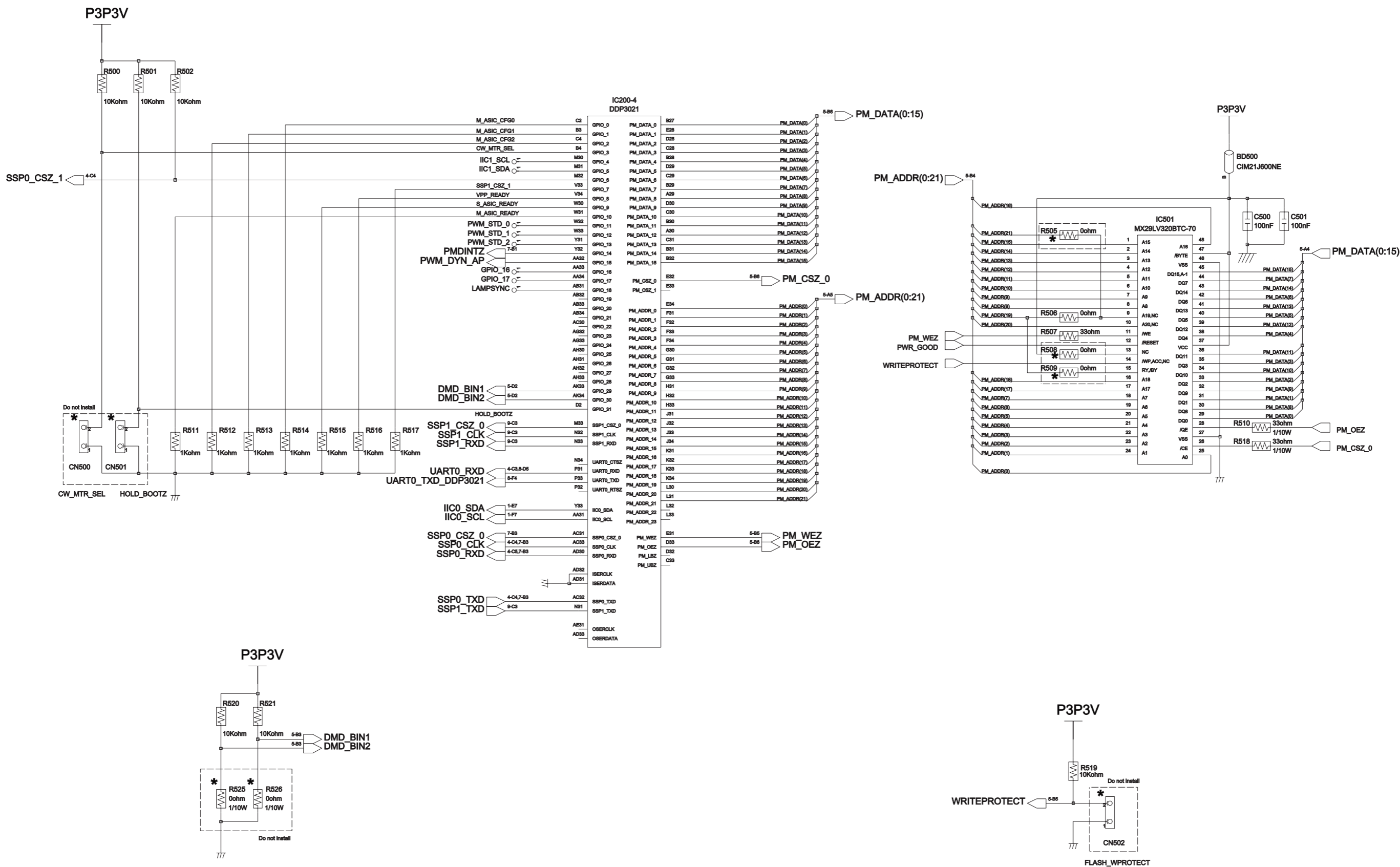
10-2-3 DMD Board-3



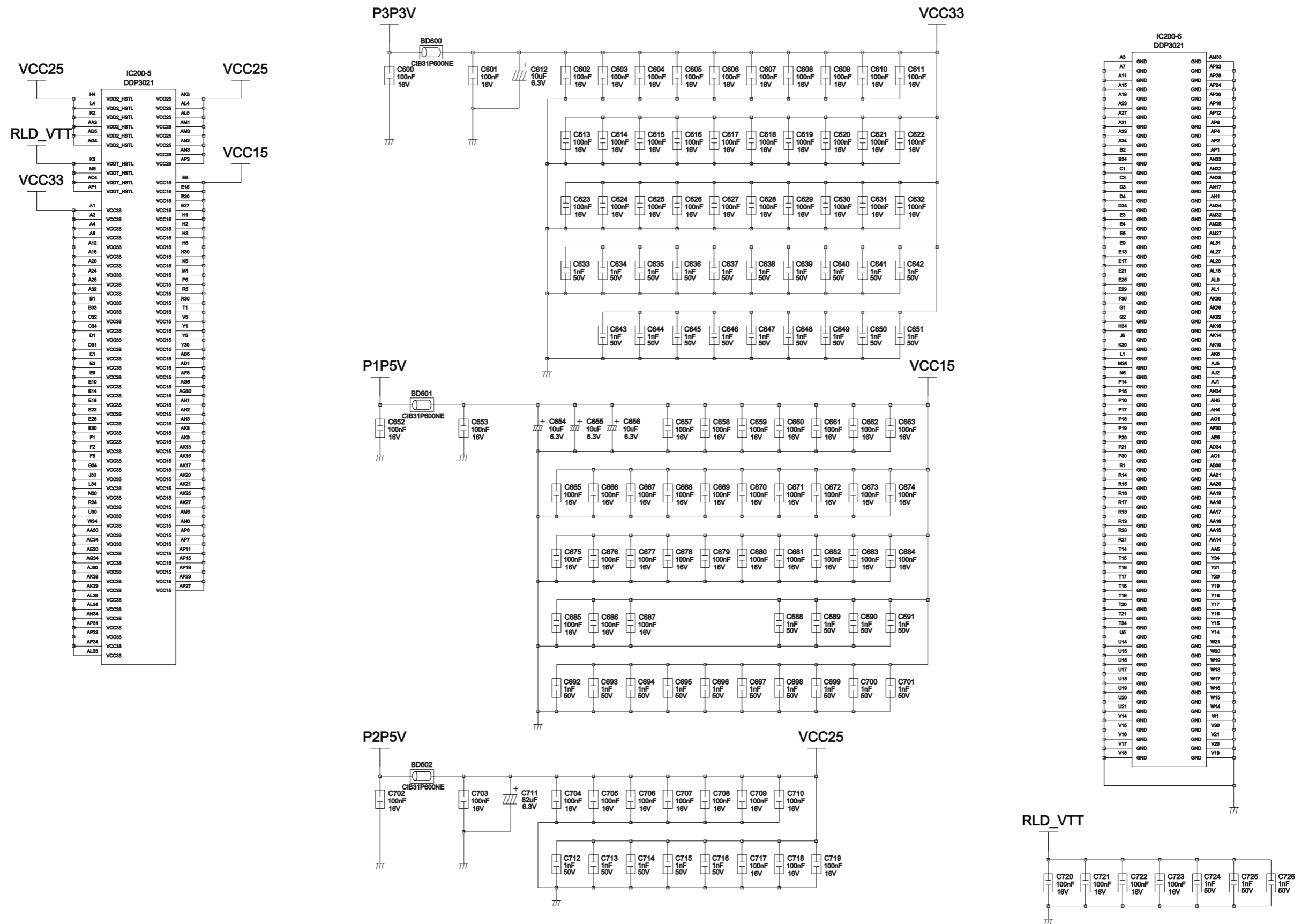
10-2-4 DMD Board-4



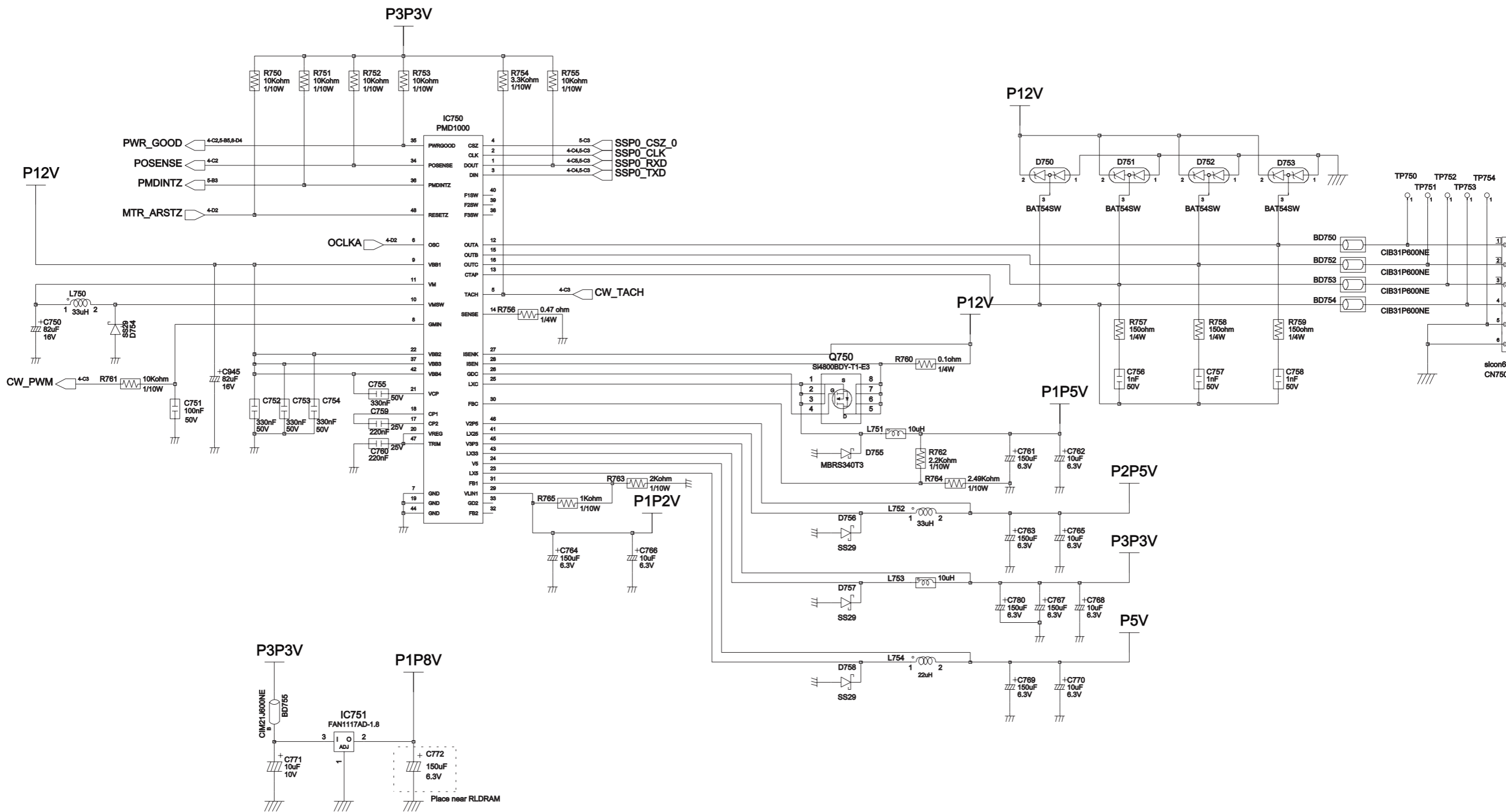
10-2-5 DMD Board-5



10-2-6 DMD Board-6

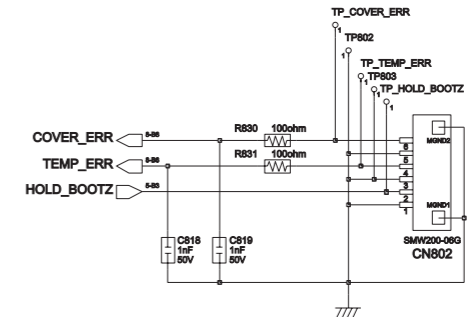
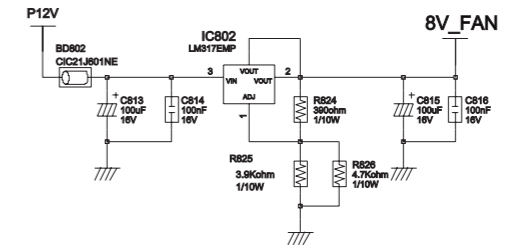
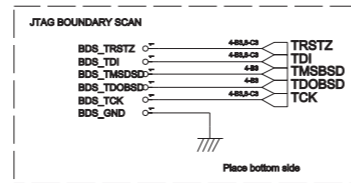
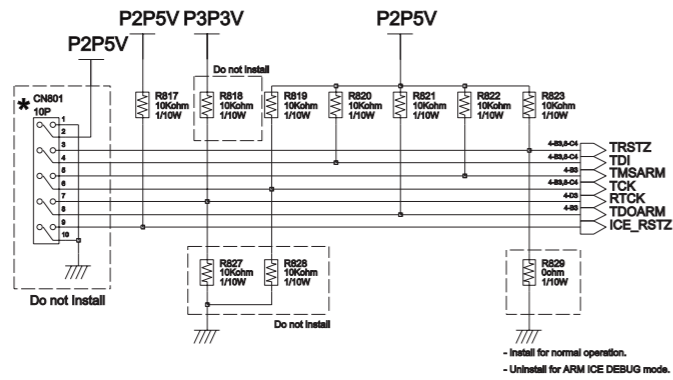
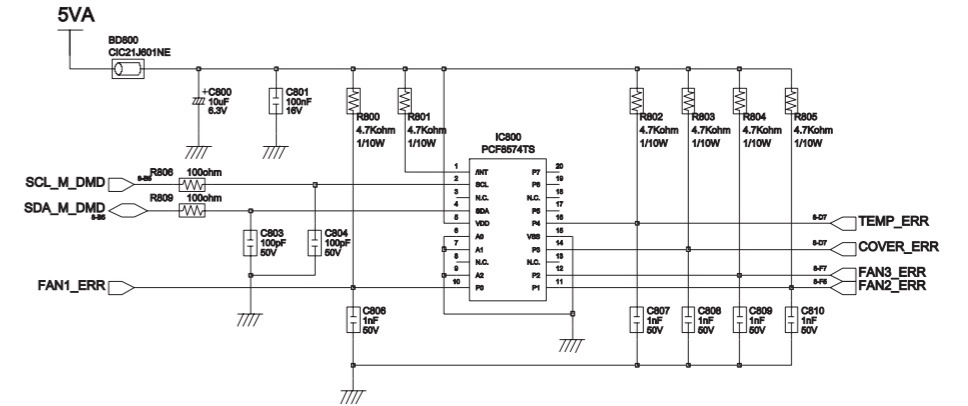
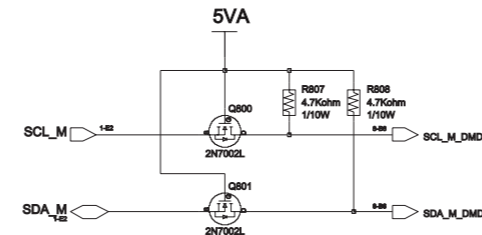
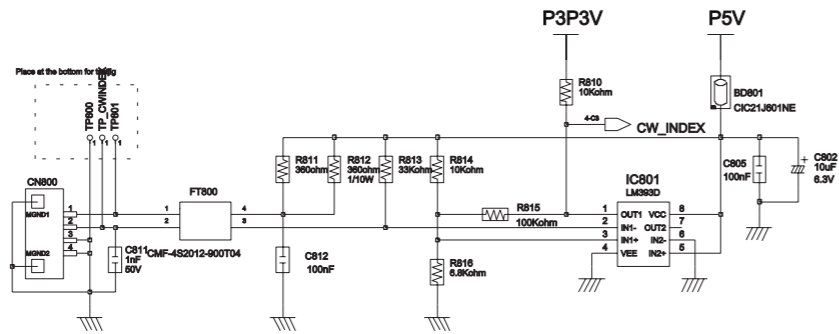


10-2-7 DMD Board-7

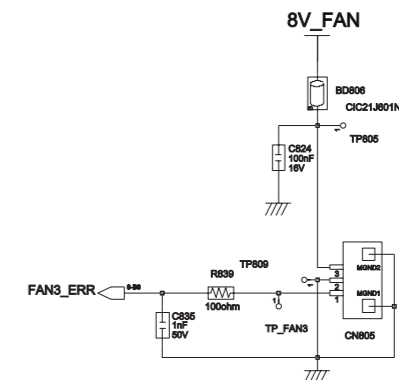
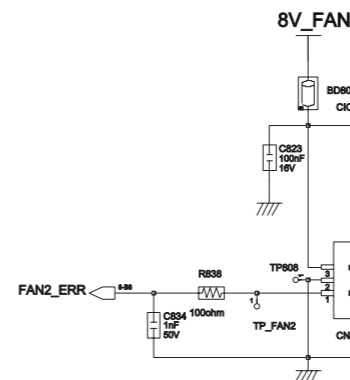
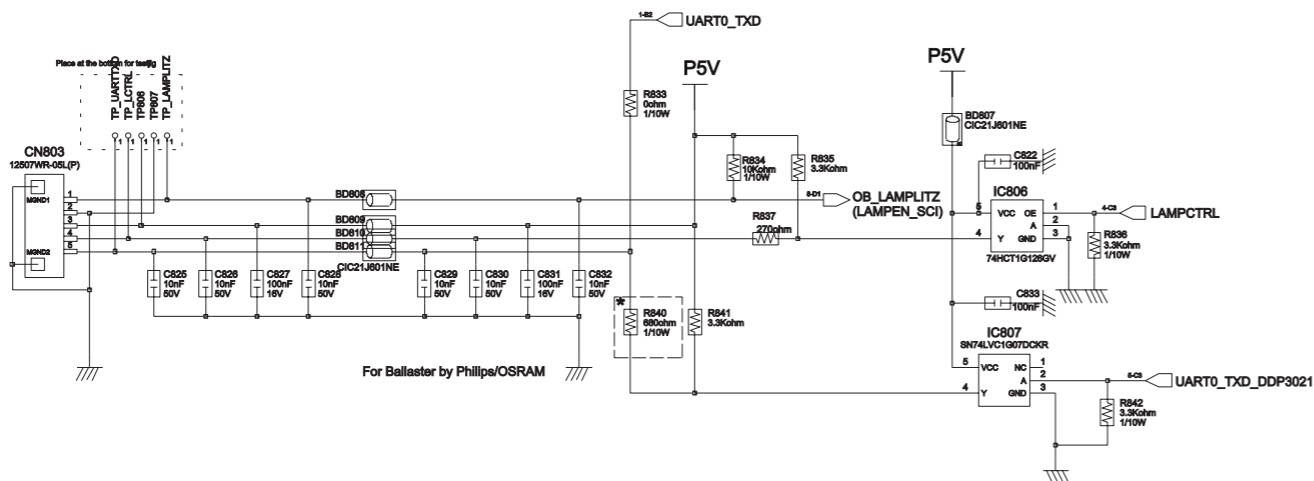
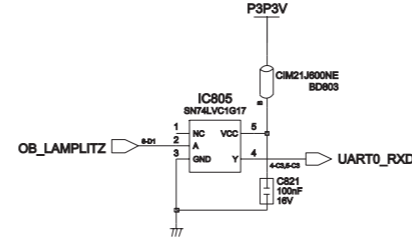
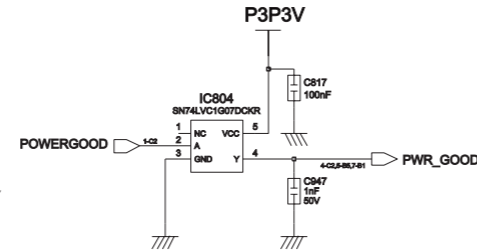
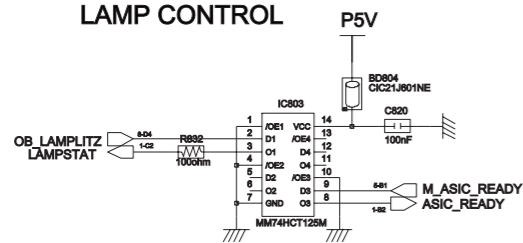


10-2-8 DMD Board-8

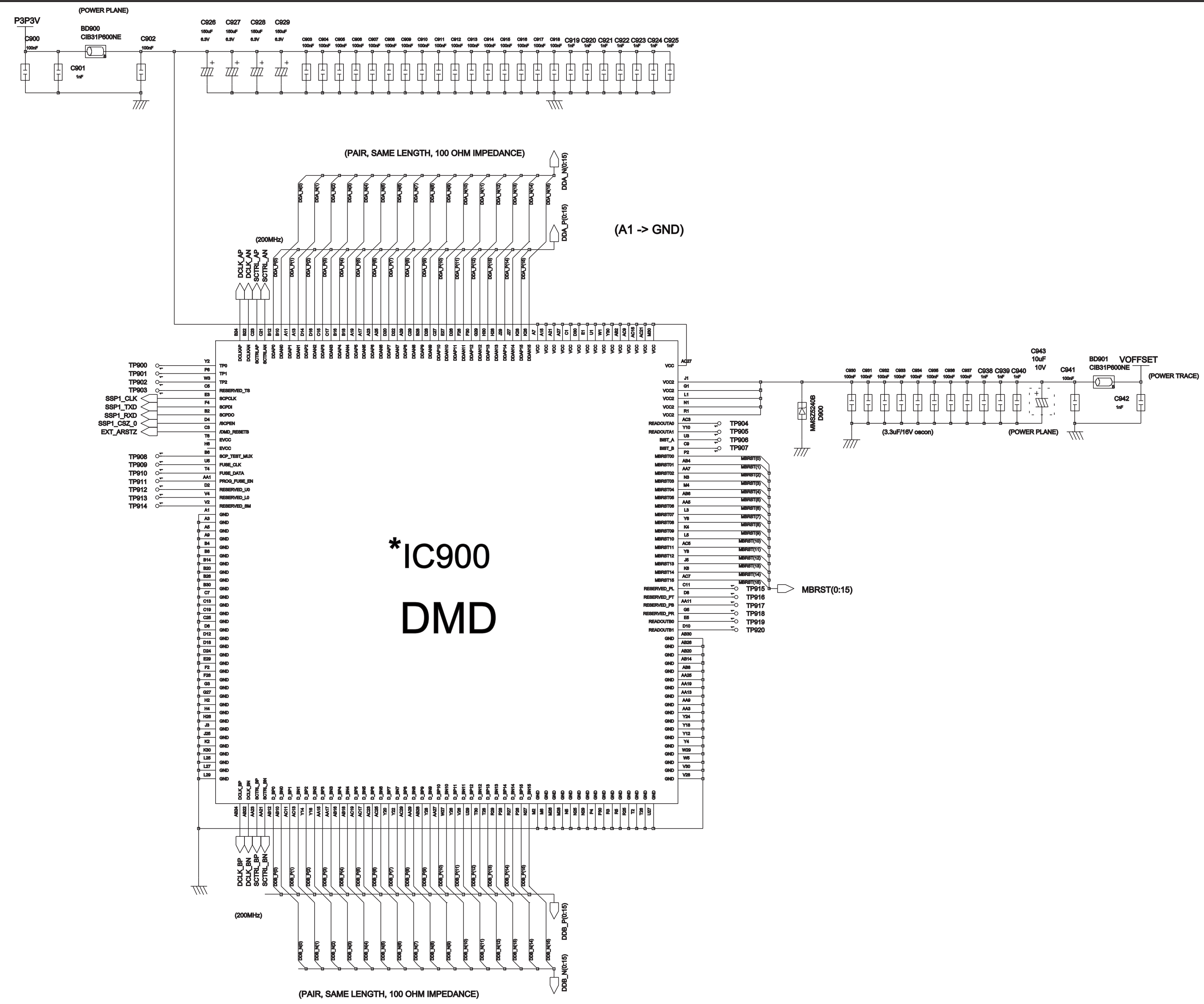
COLOR WHEEL SENSOR DETECTION



LAMP CONTROL

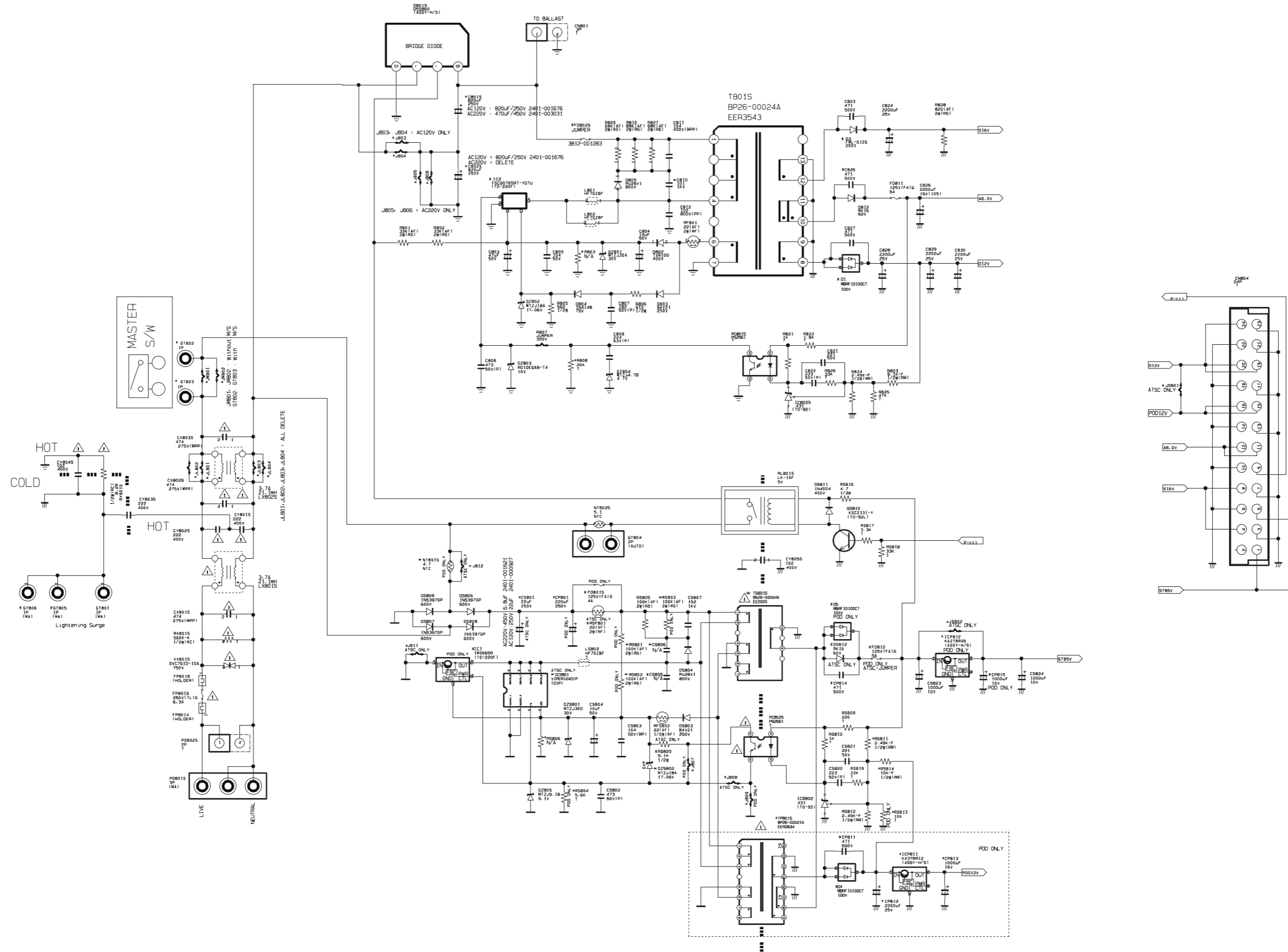


10-2-9 DMD Board-9

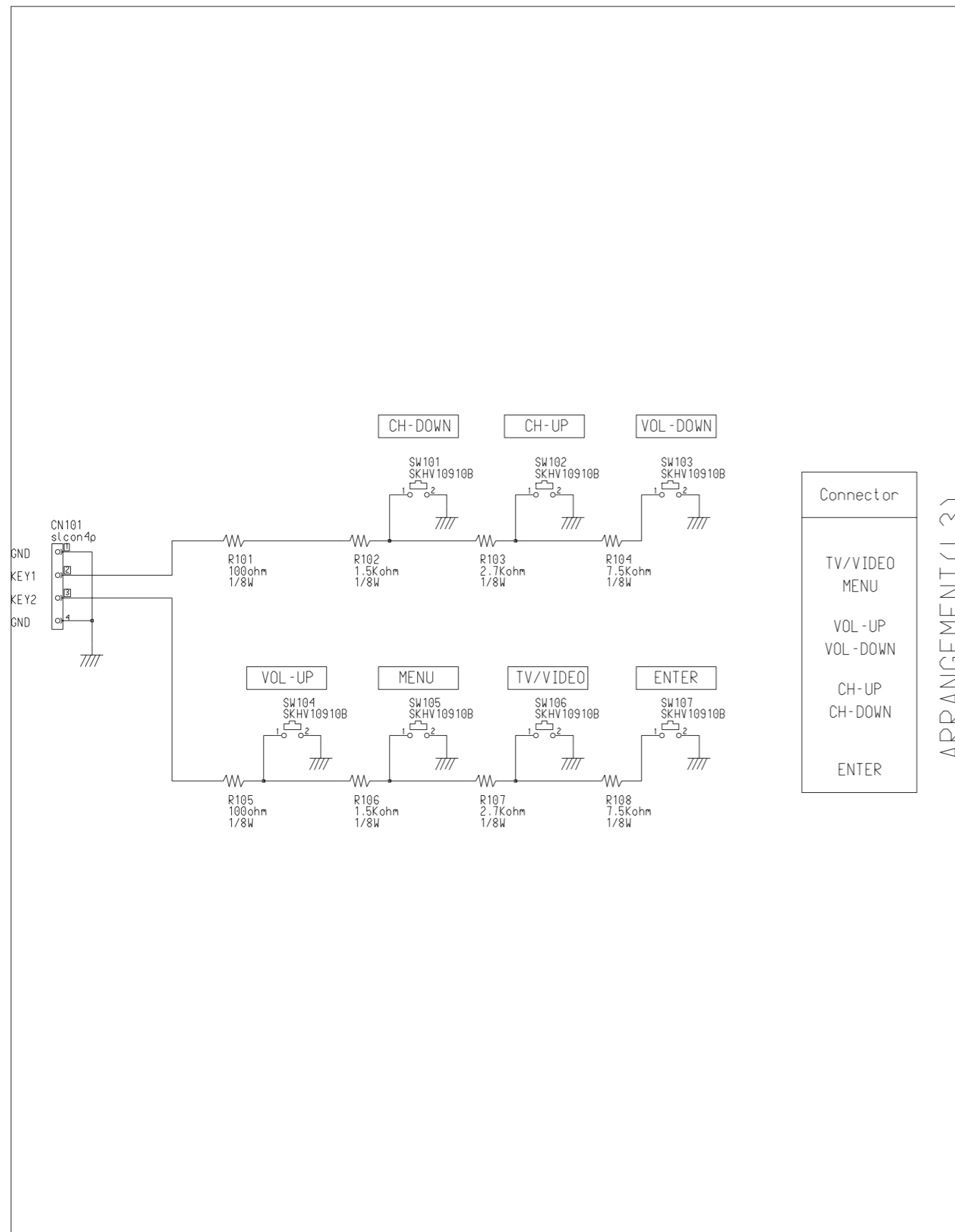


*IC900
DMD

10-3 Power Board



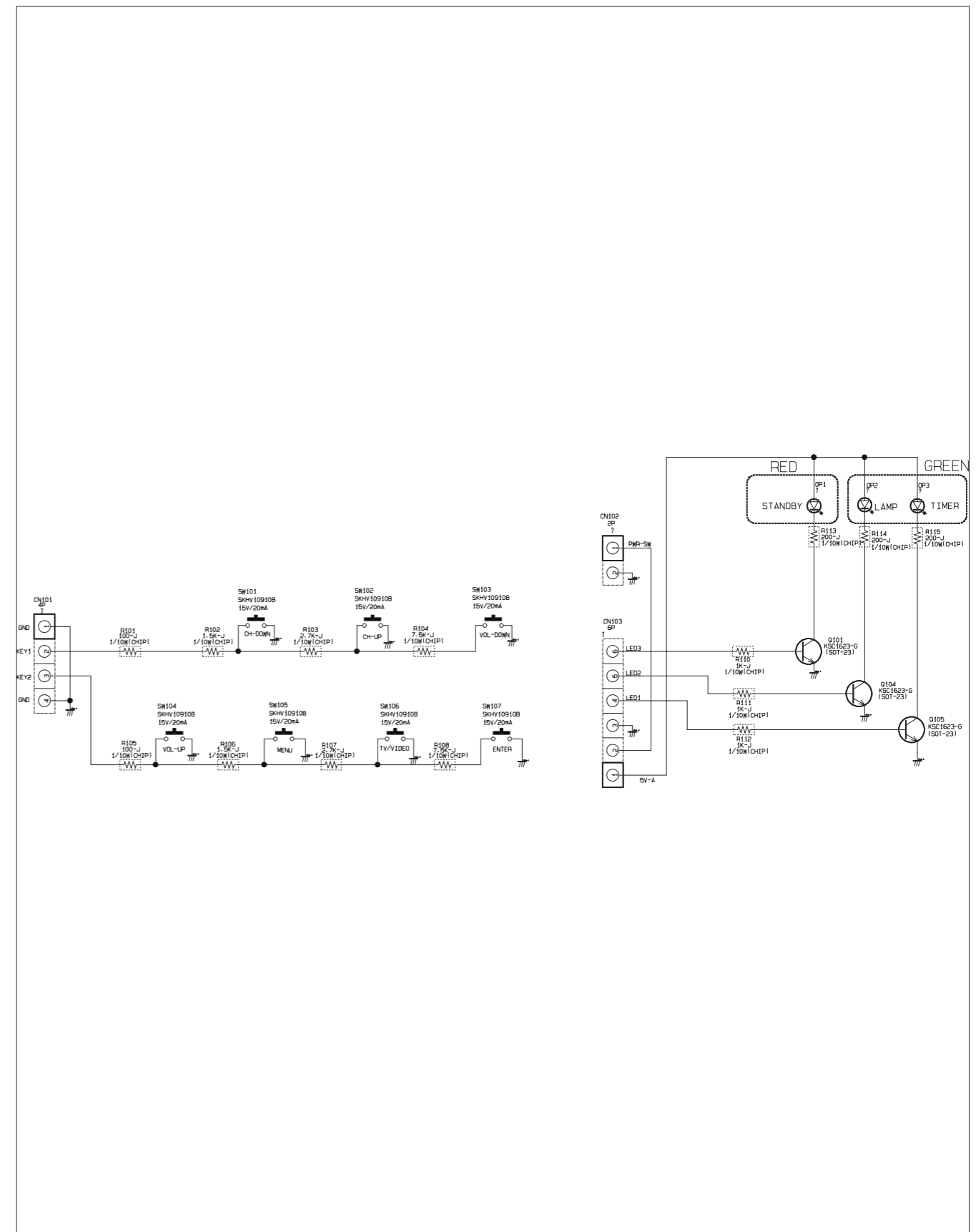
10-4 Key Control (L6)



Connector
TV/VIDEO MENU
VOL-UP VOL-DOWN
CH-UP CH-DOWN
ENTER

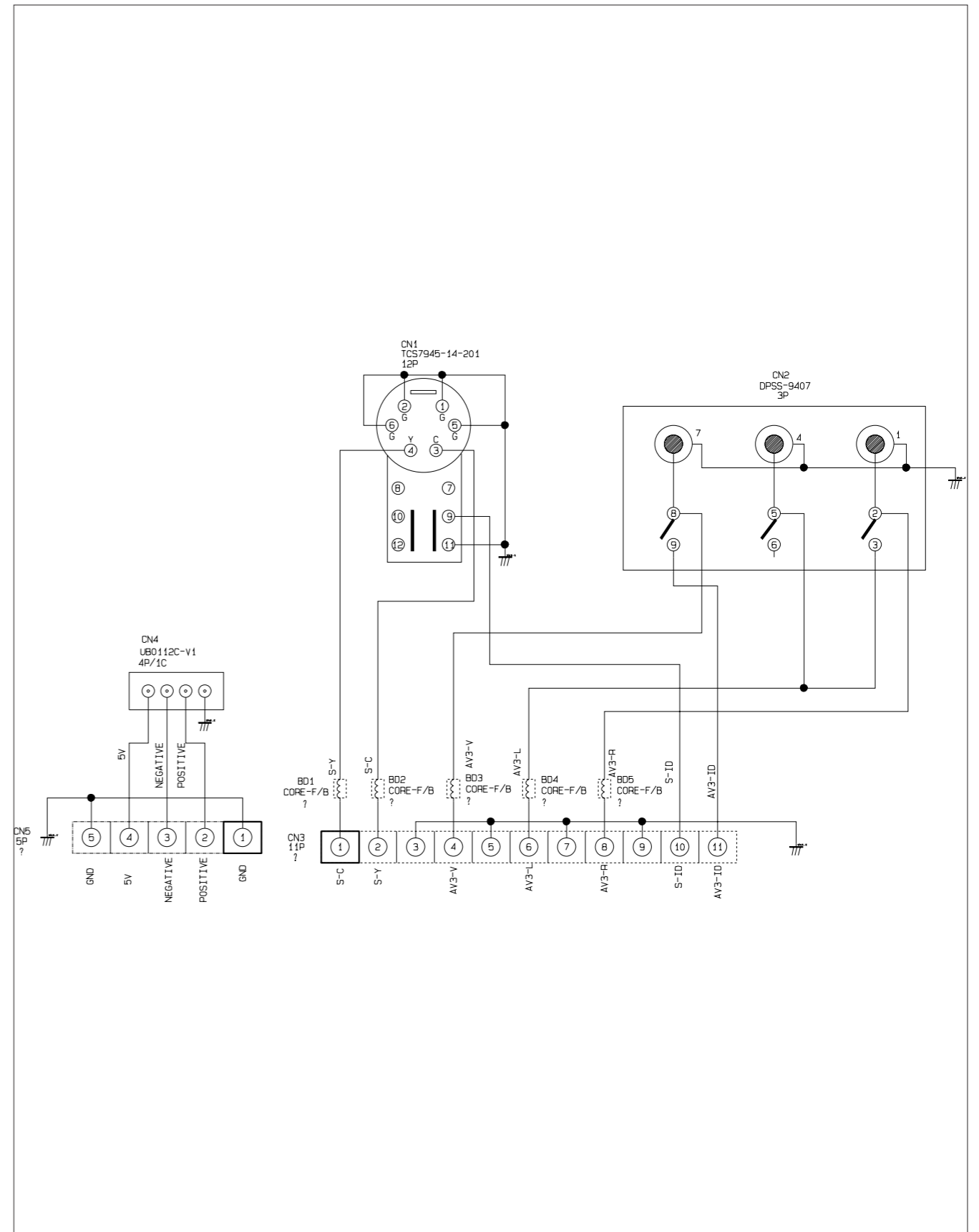
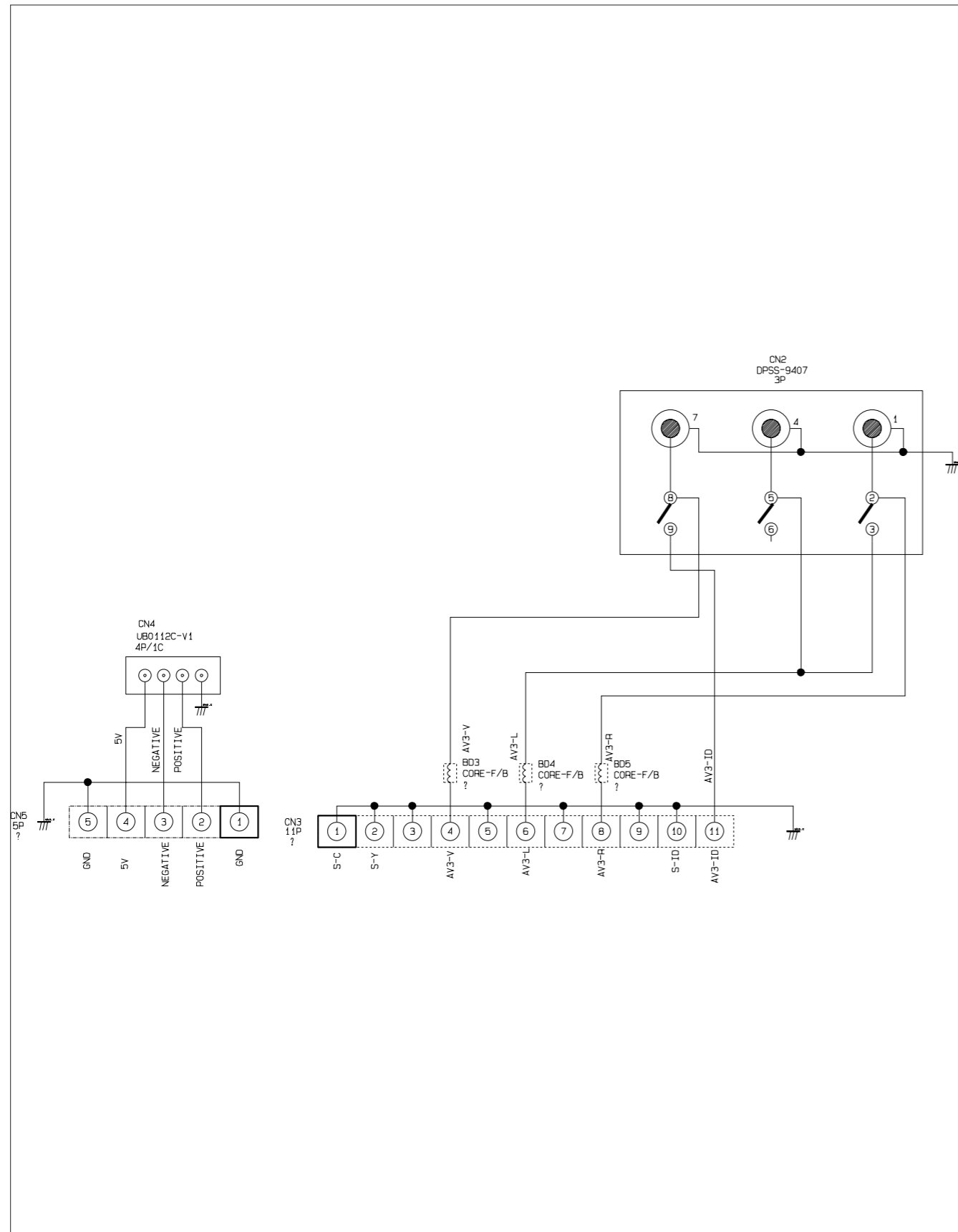
ARRANGEMENT (L3)

10-4 Key Control (K2)

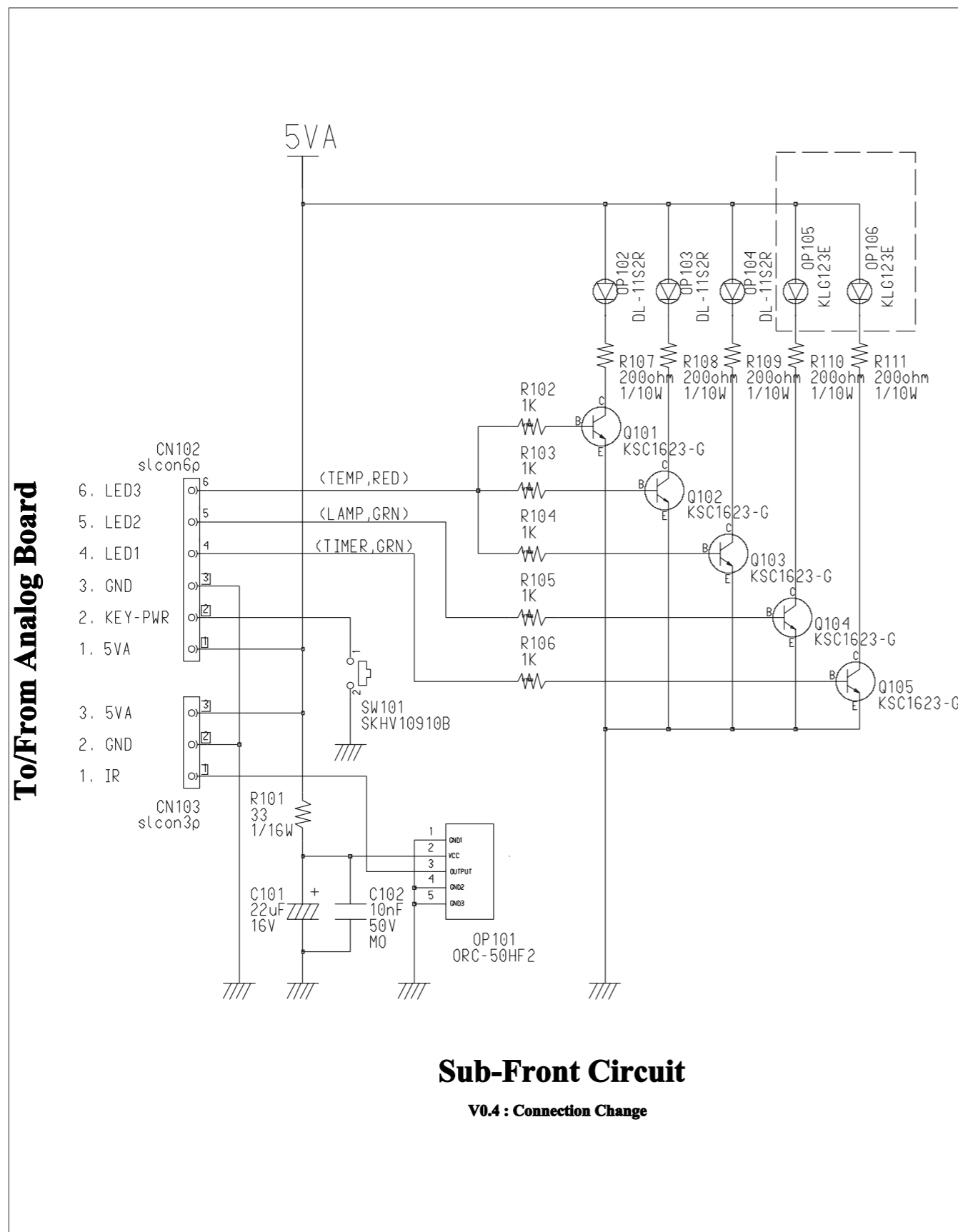


10-5 Side-AV (L6)

10-5 Side-AV (K2)



10-6 RMC LED (L6)



10-6 RMC (K2)

